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## 1 Introduction and functional overview

The SPI Handler/Driver provides services for reading from and writing to devices connected via SPI busses. It provides access to SPI communication to several users (e.g. EEPROM, Watchdog, I/O ASICs). It also provides the required mechanism to configure the onchip SPI peripheral.

This specification describes the API for a monolithic SPI Handler/Driver. This software module includes handling and driving functionalities. Main objectives of this monolithic SPI Handler/Driver are to take the best of each microcontroller features and to allow implementation optimization depending on static configuration to fit as much as possible to ECU needs.

Hence, this specification defines selectable levels of functionalities and configurable features to allow the design of a high scalable module that exploits the peculiarities of the microcontroller.

To configure the SPI Handler/Driver these steps shall be followed:

- SPI Handler/Driver Level of Functionality shall be selected and optional features configured.
- SPI Channels shall be defined according to data usage, and they could be buffered inside the SPI Handler/Driver (IB) or provided by the user (EB).
- SPI Jobs shall be defined according to HW properties (CS), and they will contain a list of channels using those properties.
- As a final step, Sequences of Jobs shall be defined, in order to transmit data in a sorted way (priority sorted).

The general behaviour of the SPI Handler/Driver can be asynchronous or synchronous according to the Level of Functionality selected.

The specification covers the Handler/Driver functionality combined in one single module. One is the SPI handling part that handles multiple access to busses that could be located in the ECU Abstraction layer. The other part is the SPI driver that accesses the microcontroller hardware directly that could be located in the Microcontroller Abstraction layer.

## 2 Acronyms and abbreviations

Acronyms and abbreviations which have a local scope and therefore are not contained in the AUTOSAR glossary must appear in a local glossary.

<b>Acronym:</b>	<b>Description:</b>
DET	Default Error Tracer – module to which errors are reported.
DEM	Diagnostic Event Manager – module to which production relevant errors are reported.
SPI	Serial Peripheral Interface. It is exactly defined hereafter in this document.
CS	Chip Select
MISO	Master Input Slave Output
MOSI	Master Output Slave Input

<b>Abbreviation:</b>	<b>Description:</b>
EB	Externally buffered channels. Buffers containing data to transfer are outside the SPI Handler/Driver.
IB	Internally buffered channels. Buffers containing data to transfer are inside the SPI Handler/Driver.
ID	Identification Number of an element (Channel, Job, Sequence).

<b>Definition:</b>	<b>Description:</b>
Channel	A Channel is a software exchange medium for data that are defined with the same criteria: Config. Parameters, Number of Data elements with same size and data pointers (Source & Destination) or location.
Job	A Job is composed of one or several Channels with the same Chip Select (is not released during the processing of Job). A Job is considered atomic and therefore cannot be interrupted by another Job. A Job has an assigned priority.
Sequence	A Sequence is a number of consecutive Jobs to transmit but it can be rescheduled between Jobs using a priority mechanism. A Sequence transmission is interruptible (by another Sequence transmission) or not depending on a static configuration.

## 3 Related documentation

### 3.1 Input documents

- [1] Layered Software Architecture  
AUTOSAR\_EXP\_LayeredSoftwareArchitecture.pdf
- [2] General Requirements on SPAL  
AUTOSAR\_SRS\_SPALGeneral.pdf
- [3] General Requirements on Basic Software Modules  
AUTOSAR\_SRS\_BSWGeneral.pdf
- [4] Specification of Default Error Tracer  
AUTOSAR\_SWS\_DefaultErrorTracer.pdf
- [5] Specification of ECU Configuration  
AUTOSAR\_TPS\_ECUConfiguration.pdf
- [6] Requirements on SPI Handler/Driver  
AUTOSAR\_SRS\_SPIHandlerDriver.pdf
- [7] Specification of Diagnostic Event Manager  
AUTOSAR\_SWS\_DiagnosticEventManager.pdf
- [8] Glossary  
AUTOSAR\_TR\_Glossary.pdf
- [9] Specification of MCU Driver  
AUTOSAR\_SWS\_MCUDriver .pdf
- [10] Specification of PORT Driver  
AUTOSAR\_SWS\_PORTDriver
- [11] Basic Software Module Description Template,  
AUTOSAR\_TPS\_BSWModuleDescriptionTemplate.pdf
- [12] List of Basic Software Modules  
AUTOSAR\_TR\_BSWModuleList
- [13] Specification of Standard Types,  
AUTOSAR\_SWS\_StandardTypes.pdf
- [14] General Specification of Basic Software Modules  
AUTOSAR\_SWS\_BSWGeneral.pdf

### 3.2 Related standards and norms

Not related.

### 3.3 Related specification

AUTOSAR provides a General Specification on Basic Software modules [14] (SWS BSW General), which is also valid for SPI Handler Driver.

Thus, the specification SWS BSW General shall be considered as additional and required specification for SPI Handler Driver.

## 4 Constraints and assumptions

### 4.1 Limitations

**[SWS\_Spi\_00040]** 「The SPI Handler/Driver handles only the Master mode.」()

**[SWS\_Spi\_00050]** 「The SPI Handler/Driver only supports full-duplex mode.」()

**[SWS\_Spi\_00108]** 「The LEVEL 2 SPI Handler/Driver is specified for microcontrollers that have to provide, at least, two SPI busses using separated hardware units. Otherwise, using this level of functionality does not make sense.」()

### 4.2 Applicability to car domains

No restrictions.

## 5 Dependencies to other modules

**[SWS\_Spi\_00244]** 「The SPI Handler/Driver module does not take care of setting the registers which configure the clock, prescaler(s) and PLL in its init function. This has to be done by the MCU module [9].」()

Note: SPI peripherals may depend on the system clock, prescaler(s) and PLL. Thus, any change of the system clock (e.g. PLL on / PLL off / clock dividers) may also affect the clock settings of the SPI hardware.

**[SWS\_Spi\_00342]** 「Depending on microcontrollers, the SPI peripheral could share registers with other peripherals. In this typical case, the SPI Handler/Driver has a relationship with MCU module [9] for initialising and de-initialising those registers.」()

**[SWS\_Spi\_00343]** 「If Chip Selects are done using microcontroller pins the SPI Handler/Driver has a relationship with PORT module [10]. In this case, this specification assumes that these microcontroller pins are directly accessed by the SPI Handler/Driver module without using APIs of DIO module. Anyhow, the SPI depends on ECU hardware design and for that reason it may depend on other modules.」()

## 6 Requirements traceability

Requirement	Description	Satisfied by
SRS_BSW_00005	Modules of the $\mu$ C Abstraction Layer (MCAL) may not have hard coded horizontal interfaces	SWS_Spi_00999
SRS_BSW_00006	The source code of software modules above the $\mu$ C Abstraction Layer (MCAL) shall not be processor and compiler dependent.	SWS_Spi_00999
SRS_BSW_00009	All Basic SW Modules shall be documented according to a common standard.	SWS_Spi_00999
SRS_BSW_00010	The memory consumption of all Basic SW Modules shall be documented for a defined configuration for all supported platforms.	SWS_Spi_00999
SRS_BSW_00101	The Basic Software Module shall be able to initialize variables and hardware in a separate initialization function	SWS_Spi_00013, SWS_Spi_00015
SRS_BSW_00161	The AUTOSAR Basic Software shall provide a microcontroller abstraction layer which provides a standardized interface to higher software layers	SWS_Spi_00999
SRS_BSW_00164	The Implementation of interrupt service routines shall be done by the Operating System, complex drivers or modules	SWS_Spi_00999
SRS_BSW_00168	SW components shall be tested by a function defined in a common API in the Basis-SW	SWS_Spi_00999
SRS_BSW_00170	The AUTOSAR SW Components shall provide information about their dependency from faults, signal qualities, driver demands	SWS_Spi_00999
SRS_BSW_00172	The scheduling strategy that is built inside the Basic Software Modules shall be compatible with the strategy used in the system	SWS_Spi_00999
SRS_BSW_00301	All AUTOSAR Basic Software Modules shall only import the necessary information	SWS_Spi_00999
SRS_BSW_00302	All AUTOSAR Basic Software Modules shall only export information needed by other modules	SWS_Spi_00999
SRS_BSW_00306	AUTOSAR Basic Software Modules shall be compiler and platform independent	SWS_Spi_00999
SRS_BSW_00307	Global variables naming convention	SWS_Spi_00999

SRS_BSW_00308	AUTOSAR Basic Software Modules shall not define global data in their header files, but in the C file	SWS_Spi_00999
SRS_BSW_00309	All AUTOSAR Basic Software Modules shall indicate all global data with read-only purposes by explicitly assigning the const keyword	SWS_Spi_00999
SRS_BSW_00312	Shared code shall be reentrant	SWS_Spi_00999
SRS_BSW_00323	All AUTOSAR Basic Software Modules shall check passed API parameters for validity	SWS_Spi_00031, SWS_Spi_00032, SWS_Spi_00060
SRS_BSW_00325	The runtime of interrupt service routines and functions that are running in interrupt context shall be kept short	SWS_Spi_00999
SRS_BSW_00327	Error values naming convention	SWS_Spi_00004
SRS_BSW_00328	All AUTOSAR Basic Software Modules shall avoid the duplication of code	SWS_Spi_00999
SRS_BSW_00330	It shall be allowed to use macros instead of functions where source code is used and runtime is critical	SWS_Spi_00999
SRS_BSW_00331	All Basic Software Modules shall strictly separate error and status information	SWS_Spi_00999
SRS_BSW_00334	All Basic Software Modules shall provide an XML file that contains the meta data	SWS_Spi_00999
SRS_BSW_00335	Status values naming convention	SWS_Spi_00019, SWS_Spi_00061, SWS_Spi_00062, SWS_Spi_00373
SRS_BSW_00336	Basic SW module shall be able to shutdown	SWS_Spi_00021, SWS_Spi_00022
SRS_BSW_00337	Classification of development errors	SWS_Spi_00004
SRS_BSW_00341	Module documentation shall contains all needed informations	SWS_Spi_00999
SRS_BSW_00342	It shall be possible to create an AUTOSAR ECU out of modules provided as source code and modules provided as object code, even mixed	SWS_Spi_00999
SRS_BSW_00343	The unit of time for specification and configuration of Basic SW modules shall be preferably in physical time unit	SWS_Spi_00999
SRS_BSW_00347	A Naming separation of different instances of BSW drivers shall be in place	SWS_Spi_00999
SRS_BSW_00359	All AUTOSAR Basic Software Modules callback functions shall avoid return types other than void if possible	SWS_Spi_00048
SRS_BSW_00360	AUTOSAR Basic Software Modules callback functions are allowed to have parameters	SWS_Spi_00048

SRS_BSW_00369	All AUTOSAR Basic Software Modules shall not return specific development error codes via the API	SWS_Spi_00048
SRS_BSW_00375	Basic Software Modules shall report wake-up reasons	SWS_Spi_00999
SRS_BSW_00385	List possible error notifications	SWS_Spi_00004
SRS_BSW_00399	Parameter-sets shall be located in a separate segment and shall be loaded after the code	SWS_Spi_00999
SRS_BSW_00400	Parameter shall be selected from multiple sets of parameters after code has been loaded and started	SWS_Spi_00999
SRS_BSW_00401	Documentation of multiple instances of configuration parameters shall be available	SWS_Spi_00999
SRS_BSW_00405	BSW Modules shall support multiple configuration sets	SWS_Spi_00013
SRS_BSW_00406	A static status variable denoting if a BSW module is initialized shall be initialized with value 0 before any APIs of the BSW module is called	SWS_Spi_00015, SWS_Spi_00046, SWS_Spi_00373
SRS_BSW_00413	An index-based accessing of the instances of BSW modules shall be done	SWS_Spi_00999
SRS_BSW_00416	The sequence of modules to be initialized shall be configurable	SWS_Spi_00999
SRS_BSW_00417	Software which is not part of the SW-C shall report error events only after the DEM is fully operational.	SWS_Spi_00999
SRS_BSW_00422	Pre-de-bouncing of error status information is done within the DEM	SWS_Spi_00999
SRS_BSW_00423	BSW modules with AUTOSAR interfaces shall be describable with the means of the SW-C Template	SWS_Spi_00999
SRS_BSW_00424	BSW module main processing functions shall not be allowed to enter a wait state	SWS_Spi_00999
SRS_BSW_00426	BSW Modules shall ensure data consistency of data which is shared between BSW modules	SWS_Spi_00999
SRS_BSW_00427	ISR functions shall be defined and documented in the BSW module description template	SWS_Spi_00999
SRS_BSW_00428	A BSW module shall state if its main processing function(s) has to be executed in a specific order or sequence	SWS_Spi_00999
SRS_BSW_00429	Access to OS is restricted	SWS_Spi_00999
SRS_BSW_00432	Modules should have separate main processing functions for read/receive and write/transmit data path	SWS_Spi_00999

SRS_BSW_00433	Main processing functions are only allowed to be called from task bodies provided by the BSW Scheduler	SWS_Spi_00999
SRS_SPAL_00157	All drivers and handlers of the AUTOSAR Basic Software shall implement notification mechanisms of drivers and handlers	SWS_Spi_00026, SWS_Spi_00038, SWS_Spi_00042, SWS_Spi_00057, SWS_Spi_00071, SWS_Spi_00073, SWS_Spi_00075, SWS_Spi_00324
SRS_SPAL_12056	All driver modules shall allow the static configuration of notification mechanism	SWS_Spi_00044, SWS_Spi_00054
SRS_SPAL_12057	All driver modules shall implement an interface for initialization	SWS_Spi_00013, SWS_Spi_00015
SRS_SPAL_12063	All driver modules shall only support raw value mode	SWS_Spi_00999
SRS_SPAL_12064	All driver modules shall raise an error if the change of the operation mode leads to degradation of running operations	SWS_Spi_00021, SWS_Spi_00025
SRS_SPAL_12067	All driver modules shall set their wake-up conditions depending on the selected operation mode	SWS_Spi_00999
SRS_SPAL_12068	The modules of the MCAL shall be initialized in a defined sequence	SWS_Spi_00999
SRS_SPAL_12069	All drivers of the SPAL that wake up from a wake-up interrupt shall report the wake-up reason	SWS_Spi_00999
SRS_SPAL_12075	All drivers with random streaming capabilities shall use application buffers	SWS_Spi_00053
SRS_SPAL_12077	All drivers shall provide a non blocking implementation	SWS_Spi_00999
SRS_SPAL_12078	The drivers shall be coded in a way that is most efficient in terms of memory and runtime resources	SWS_Spi_00999
SRS_SPAL_12092	The driver's API shall be accessed by its handler or manager	SWS_Spi_00999
SRS_SPAL_12125	All driver modules shall only initialize the configured resources	SWS_Spi_00013
SRS_SPAL_12129	The ISRs shall be responsible for resetting the interrupt flags and calling the according notification function	SWS_Spi_00999
SRS_SPAL_12163	All driver modules shall implement an interface for de-initialization	SWS_Spi_00021, SWS_Spi_00022
SRS_SPAL_12265	Configuration data shall be kept constant	SWS_Spi_00999
SRS_SPAL_12267	Wakeup sources shall be initialized by MCAL drivers and/or the MCU driver	SWS_Spi_00999
SRS_Spi_12025	The SPI Handler/Driver shall allow the static configuration of all software and hardware properties related to SPI	SWS_Spi_00052, SWS_Spi_00053

SRS_Spi_12032	For an SPI channel assigned to an SPI HW Unit the chip select mode "normal" shall be available	SWS_Spi_00066
SRS_Spi_12033	For an SPI channel assigned to an SPI HW Unit the chip select mode "hold" shall be available	SWS_Spi_00066
SRS_Spi_12037	The SPI Handler/Driver shall allow a priority controlled allocation of the HW SPI unit	SWS_Spi_00014, SWS_Spi_00059, SWS_Spi_00124, SWS_Spi_00127
SRS_Spi_12093	The SPI Handler/Driver shall be able to handle multiple busses of communication	SWS_Spi_00034, SWS_Spi_00041, SWS_Spi_00135
SRS_Spi_12094	The SPI Handler/Driver shall handle the chip select	SWS_Spi_00066
SRS_Spi_12099	The SPI Handler/Driver shall provide an asynchronous read functionality	SWS_Spi_00016, SWS_Spi_00020, SWS_Spi_00162, SWS_Spi_00163
SRS_Spi_12101	The SPI Handler/Driver shall provide an asynchronous write functionality	SWS_Spi_00018, SWS_Spi_00020, SWS_Spi_00162, SWS_Spi_00163
SRS_Spi_12103	The SPI Handler/Driver shall provide an asynchronous read-write functionality	SWS_Spi_00020, SWS_Spi_00053, SWS_Spi_00058, SWS_Spi_00067, SWS_Spi_00162, SWS_Spi_00163
SRS_Spi_12104	The SPI Handler/Driver shall provide a synchronous functionality which returns any transfer status	SWS_Spi_00025, SWS_Spi_00026, SWS_Spi_00324
SRS_Spi_12108	The SPI Handler/Driver shall call the statically configured notification function	SWS_Spi_00057, SWS_Spi_00118, SWS_Spi_00119, SWS_Spi_00120
SRS_Spi_12150	The SPI Handler/Driver shall allow the static configuration of all software and hardware properties related to asynchronous SPI aspects	SWS_Spi_00093
SRS_Spi_12152	The SPI Handler/Driver shall provide a synchronous read functionality	SWS_Spi_00016, SWS_Spi_00134
SRS_Spi_12153	The SPI Handler/Driver shall provide a synchronous write functionality	SWS_Spi_00018, SWS_Spi_00134
SRS_Spi_12154	The SPI Handler/Driver shall provide a synchronous write-read functionality	SWS_Spi_00134
SRS_Spi_12170	The SPI Handler/Driver shall not provide the ability to prevent a channel data overwrite	SWS_Spi_00042, SWS_Spi_00084
SRS_Spi_12179	The SPI Handler/Driver shall allow linking consecutive SPI channels by static configuration	SWS_Spi_00003, SWS_Spi_00065
SRS_Spi_12180	The SPI Driver shall access the SPI bus only for the channel	SWS_Spi_00003, SWS_Spi_00065
SRS_Spi_12181	If an SPI access request for a linked channel is performed, the SPI Handler/Driver shall use this SPI channel and all the linked channels	SWS_Spi_00055, SWS_Spi_00065
SRS_Spi_12198	The SPI Handler/Driver shall provide	SWS_Spi_00053, SWS_Spi_00077

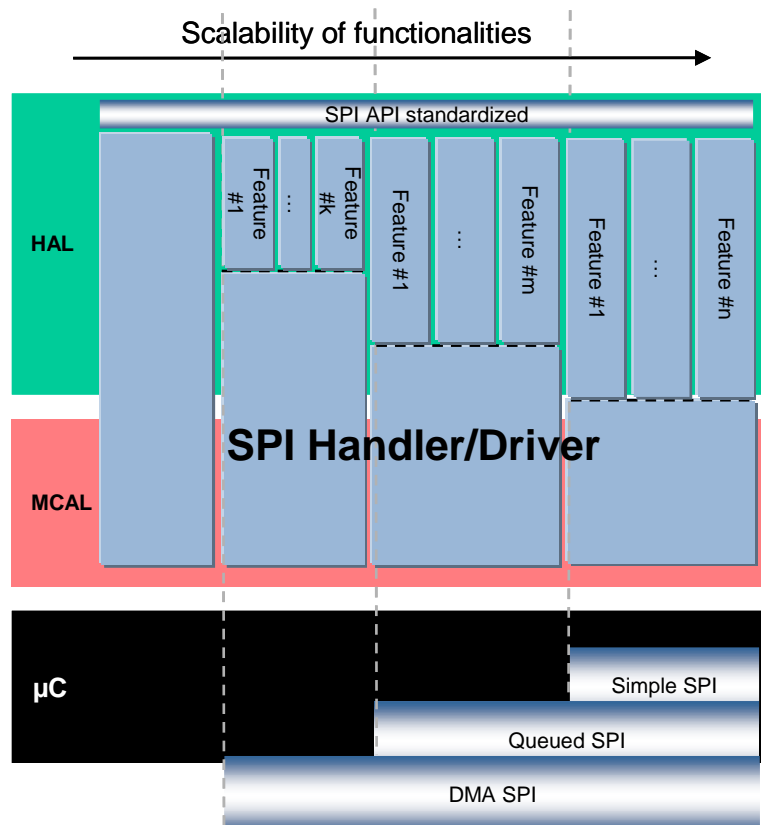
	the functionality of transferring one short data sequence with variable data content	
SRS_Spi_12199	The SPI Handler/Driver shall provide the functionality of transferring any data to any devices in one transfer sequence	SWS_Spi_00003, SWS_Spi_00065
SRS_Spi_12200	Reading large data sequences from one slave device using dummy send data shall be possible	SWS_Spi_00003, SWS_Spi_00035, SWS_Spi_00053, SWS_Spi_00065, SWS_Spi_00077
SRS_Spi_12201	Reading large data sequences from multiple slave devices using dummy send data shall be possible	SWS_Spi_00003, SWS_Spi_00035, SWS_Spi_00065, SWS_Spi_00077
SRS_Spi_12202	The SPI Handler/Driver shall support data streams to a HW device with variable number of data	SWS_Spi_00053, SWS_Spi_00078
SRS_Spi_12253	The SPI Handler/Driver shall provide the functionality of transferring one short data sequence with constant data content	SWS_Spi_00052, SWS_Spi_00078
SRS_Spi_12256	The SPI Handler/Driver shall support all controller peripherals	SWS_Spi_00034
SRS_Spi_12257	The SPI Handler/Driver shall support the communication to daisy chained HW devices	SWS_Spi_00034, SWS_Spi_00065, SWS_Spi_00066
SRS_Spi_12258	Data shall be accessible from each device individually	SWS_Spi_00003, SWS_Spi_00065
SRS_Spi_12260	Different priorities of sequences shall be supported	SWS_Spi_00002, SWS_Spi_00014, SWS_Spi_00059, SWS_Spi_00093
SRS_Spi_12261	Reading large data sequences from one slave device using variable send data shall be possible	SWS_Spi_00003, SWS_Spi_00053, SWS_Spi_00065
SRS_Spi_12262	Reading large data sequences from multiple slave devices using variable send data shall be possible	SWS_Spi_00003, SWS_Spi_00053, SWS_Spi_00065, SWS_Spi_00078
SRS_Spi_13400	The SPI Handler/Driver shall have a scalable functionality to fit the needs of the ECU	SWS_Spi_00110
SRS_Spi_13401	The SPI Handler/Driver functionalities shall be statically configurable	SWS_Spi_00109, SWS_Spi_00111, SWS_Spi_00121, SWS_Spi_00122, SWS_Spi_00125

## 7 Functional specification

The SPI (Serial Peripheral Interface) has a 4-wire synchronous serial interface. Data communication is enabled with a Chip select wire (CS). Data is transmitted with a 3-wire interface consisting of wires for serial data output (MOSI), serial data input (MISO) and serial clock (CLOCK).

### 7.1 Overall view of functionalities and features

This specification is based on previous specification experiences and also based on predominant identified use cases. The intention of this section is to summarize how the scalability of this monolithic SPI Handler/Driver allows getting a simple software module that fits simple needs up to a smart software module that fits enhanced needs.



This document specifies the following 3 Levels of Scalable Functionality for the SPI Handler/Driver:

- LEVEL 0, **Simple Synchronous SPI Handler/Driver**: the communication is based on synchronous handling with a FIFO policy to handle multiple accesses. Buffer usage is configurable to optimize and/or to take advantage of HW capabilities.

- LEVEL 1, **Basic Asynchronous SPI Handler/Driver**: the communication is based on asynchronous behavior and with a Priority policy to handle multiple accesses. Buffer usage is configurable as for “Simple Synchronous” level.
- LEVEL 2, **Enhanced (Synchronous/Asynchronous) SPI Handler/Driver**: the communication is based on asynchronous behavior or synchronous handling, using either interrupts or polling mechanism selectable during execution time and with a Priority policy to handle multiple accesses. Buffer usage is configurable as for other levels.

**[SWS\_Spi\_00109]** «The SPI Handler/Driver’s level of scalable functionality shall always be statically configurable, i.e. configured at pre-compile time to allow the best source code optimisation.»(SRS\_Spi\_13401)

**[SWS\_Spi\_00110]** «The `SpiLevelDelivered` parameter shall be configured with one of the 3 authorized values according to the described levels (0, 1 or 2) to allow the selection of the SPI Handler/Driver’s level of scalable functionality.»(SRS\_Spi\_13400)

To improve the scalability, each level has optional features which are configurable (ON / OFF) or selectable. These are described in detail in the dedicated chapters.

## 7.2 General behaviour

This chapter, on the one hand, introduces common behavior and configuration for all levels. On the other, it specifies the behavior of each level and also the allowed optional features.

**[SWS\_Spi\_00041]** «The SPI Handler/Driver interface configuration shall be based on Channels, Jobs and Sequences as defined in this document (see chapter 2).»(SRS\_Spi\_12093)

**[SWS\_Spi\_00034]** «The SPI Handler/Driver shall support one or more Channels, Jobs and Sequences to drive all kind of SPI compatible HW devices.»(SRS\_Spi\_12093, SRS\_Spi\_12256, SRS\_Spi\_12257)

**[SWS\_Spi\_00255]** «Data transmissions shall be done according to Channels, Jobs and Sequences configuration parameters.»()

**[SWS\_Spi\_00066]** «The Chip Select (CS) is attached to the Job definition.»(SRS\_Spi\_12094, SRS\_Spi\_12257, SRS\_Spi\_12032, SRS\_Spi\_12033)

**[SWS\_Spi\_00263]** 「Chip Select shall be handled during Job transmission and shall be released at the end of it. This Chip Select handling shall be done according to the Job configuration parameters.」()

**[SWS\_Spi\_00370]** 「It shall be possible to define if the Chip Select handling is managed autonomously by the HW peripheral, without explicit chip select control by the driver, or the SPI driver shall drive the chip select lines explicitly as DIO (see [ECUC\\_Spi\\_00212](#)).」()

*Example of CS handling: Set the CS active at the beginning of Job transmission; maintain it until the end of transmission of all Channels belonging to this Job afterwards set the CS inactive.*

A Channel is defined one time but it could belong to several Jobs according to the user needs and this software specification.

**[SWS\_Spi\_00065]** 「A Job shall contain at least one Channel.」(SRS\_Spi\_12257, SRS\_Spi\_12179, SRS\_Spi\_12258, SRS\_Spi\_12180, SRS\_Spi\_12181, SRS\_Spi\_12199, SRS\_Spi\_12200, SRS\_Spi\_12261, SRS\_Spi\_12201, SRS\_Spi\_12262)

**[SWS\_Spi\_00368]** 「Each Channel shall have an associated index which is used for specifying the order of the Channel within the Job.」()

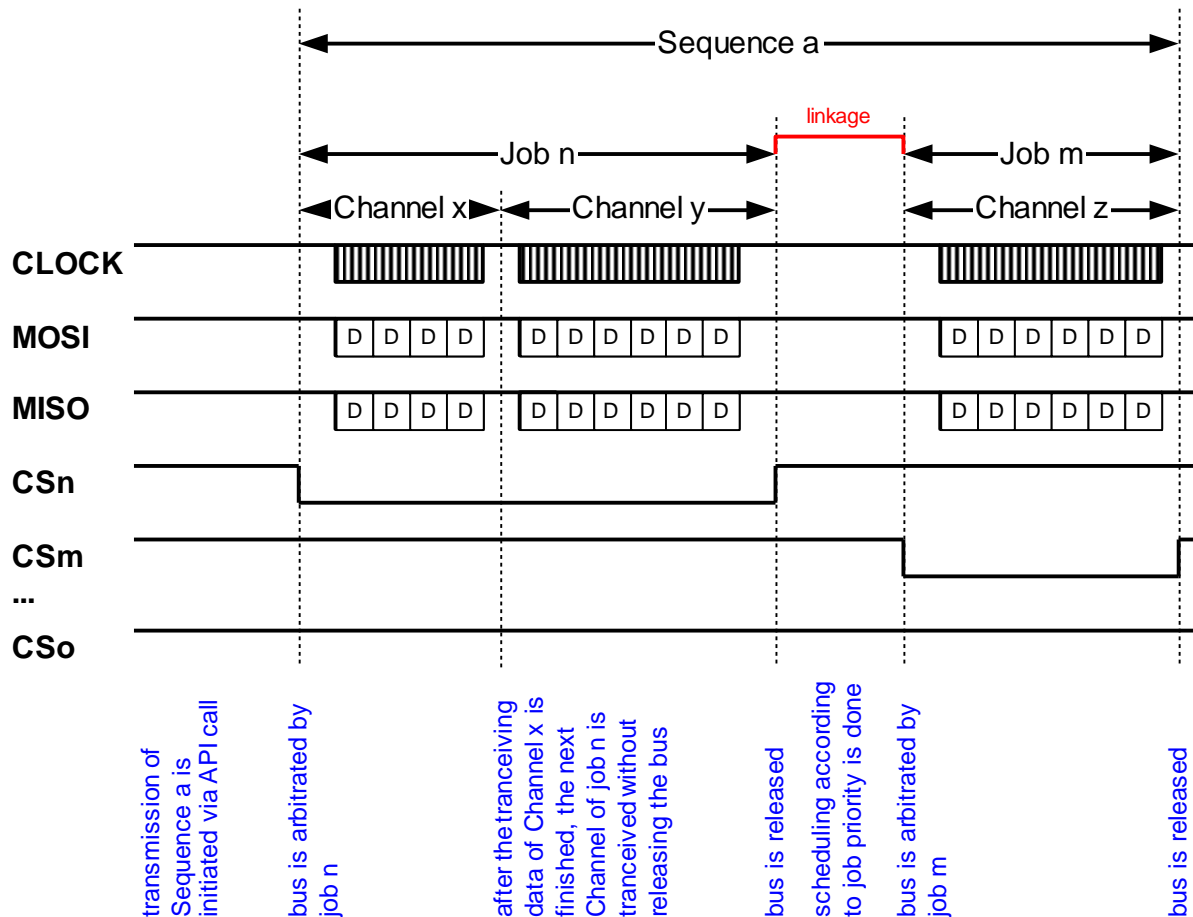
**[SWS\_Spi\_00262]** 「If a Job contains more than one Channel, all Channels contained have the same Job properties during transmission and shall be linked together statically.」()

A Job is defined one time but it could belong to several Sequences according to the user needs and this software specification.

**[SWS\_Spi\_00003]** 「A Sequence shall contain at least one Job.」(SRS\_Spi\_12179, SRS\_Spi\_12258, SRS\_Spi\_12180, SRS\_Spi\_12199, SRS\_Spi\_12200, SRS\_Spi\_12261, SRS\_Spi\_12201, SRS\_Spi\_12262)

**[SWS\_Spi\_00236]** 「If it contains more than one, all Jobs contained have the same Sequence properties during transmission and shall be linked together statically.」()

A Channel used for a transmission should have its parameters configured but it is allowed to pass Null pointers as source and destination pointers to generate a dummy transmission (See also [\[SWS\\_Spi\\_00028\]](#) & [\[SWS\\_Spi\\_00030\]](#)).



Channel data may differ from the hardware handled and user (client application) given. On the client side the data is handled in 8, 16 or 32bits mode base on SpiDataWidth (see chapter 8.2.5). On the microcontroller side, the hardware may handle between 1 and 32bits or may handle a fixed value (8 or 16bits) and this width is configurable for each Channel (see SpiDataWidth)..

**[SWS\_Spi\_00149]** 「The SPI Handler/Driver shall take care of the differences between the frame width of channel (SpiDataWidth) and data access data type (given by SWS\_Spi\_00437).」()

**[SWS\_Spi\_00289]** 「If data width (SpiDataWidth) are exactly same (8 or 16 or 32 bits), the SPI Handler/Driver can send and receive data without any bit changes straightforward.」()

**[SWS\_Spi\_00290]** If data access casting type is superior to data width (for example SpiDataWidth = 12bits, data access is 16 bits), the data transmitted through the SPI Handler/Driver shall send the lower part, ignore the upper part. Receive the lower part, extend with zero. (l)

This ensures that the user always gets the same interface.

**[SWS\_Spi\_00437]** 「Data buffers are accessed as uint8, uint16 or uint32 according to SpiDataWidth independently to Spi\_DataBufferType.

The data access will use following casting:  
uint8 for SpiDataWidth < 9  
uint16 for 9 ≤ SpiDataWidth < 17  
uint32 for 17 ≤ SpiDataWidth」()

### 7.2.1 Common configurable feature: Allowed Channel Buffers

In order to allow taking advantages of all microcontroller capabilities but also to allow sending/receiving of data to/from a dedicated memory location, all levels have an optional feature with respect to the location of Channel Buffers.

Hence, two main kinds of channel buffering can be used by configuration:

- Internally buffered Channels (IB): The buffer to transmit/receive data is provided by the Handler/Driver.
- Externally buffered Channels (EB): The buffer to transmit/receive is provided by the user (statically and/or dynamically).

Both channel buffering methods may be used depending on the 3 use cases described below:

- Usage 0: the SPI Handler/Driver manages only Internal Buffers.
- Usage 1: the SPI Handler/Driver manages only External Buffers.
- Usage 2: the SPI Handler/Driver manages both buffers types.

**[SWS\_Spi\_00111]** 「The SpiChannelBuffersAllowed parameter shall be configured with one of the 3 authorized values (0, 1 or 2) according to the described usage.」(SRS\_Spi\_13401)

**[SWS\_Spi\_00279]** 「The SpiChannelBuffersAllowed parameter shall be configured to select which Channel Buffers the SPI Handler/Driver manages.」()

#### 7.2.1.1 Behaviour of IB channels

The intention of Internal Buffer channels is to take advantage of microcontrollers including this feature by hardware. Otherwise, this feature should be simulated by software.

**[SWS\_Spi\_00052]** 「For the IB Channels, the Handler/Driver shall provide the buffering but it is not able to take care of the consistency of the data in the buffer during

transmission. The size of the Channel buffer is fixed.」(SRS\_Spi\_12025, SRS\_Spi\_12253)

**[SWS\_Spi\_00049]** 「The channel data received shall be stored in 1 entry deep internal buffers by channel. The SPI Handler/Driver shall not take care of the overwriting of these “receive” buffers by another transmission on the same channel.」()

**[SWS\_Spi\_00051]** 「The channel data to be transmitted shall be copied in 1 entry deep internal buffers by channel.」()

**[SWS\_Spi\_00257]** 「The SPI Handler/Driver is not able to prevent the overwriting of these “transmit” buffers by users during transmissions.」()

**[SWS\_Spi\_00438]** 「The Handler/Driver shall provide separate buffer for receive and transmit to ensure that transmitted data are not overwritten by the receive data.」()

### 7.2.1.2 Behaviour of EB channels

The intention of External Buffer channels is to reuse existing buffers that are located outside. That means the SPI Handler/Driver does not monitor them.

**[SWS\_Spi\_00053]** 「For EB Channels the application shall provide the buffering and shall take care of the consistency of the data in the buffer during transmission.」(SRS\_SPAL\_12075, SRS\_Spi\_12025, SRS\_Spi\_12198, SRS\_Spi\_12200, SRS\_Spi\_12261, SRS\_Spi\_12262, SRS\_Spi\_12202, SRS\_Spi\_12103)

**[SWS\_Spi\_00112]** 「The size of the Channel buffer is either fixed or variable. A maximum size for the Channel buffer shall be defined by the configuration.」()

**[SWS\_Spi\_00280]** 「The buffer provided by the application for the SPI Handler Driver may have a different size.」()

### 7.2.1.3 Buffering channel usage

The following table provides information about the Channel characteristics:

<b>IB Channels</b>	
It provides...	<ul style="list-style-type: none"> <li>• A more abstracted concept (buffering mechanisms are hidden)</li> <li>• Actual and future optimal implementation taken profit of HW buffer facilities (Given size of 256 bytes covers nowadays requirements).</li> </ul>
Suggested	<ul style="list-style-type: none"> <li>• Daisy-chain implementation.</li> </ul>

use ...	<ul style="list-style-type: none"> <li>• Small data transfer devices (up to 10 Bytes).</li> </ul>
<b>EB Channels</b>	
It provides...	<ul style="list-style-type: none"> <li>• Efficient mechanism to support large stream communication.</li> <li>• Send constant data out of ROM tables and spare RAM size.</li> <li>• Send various data tables each for a different device (highly complex ASICS with several integrated peripheral devices, also mixed signal types, could exceed IB HW buffer size)</li> </ul>
Suggested use ...	<ul style="list-style-type: none"> <li>• Large streams communication.</li> <li>• EEPROM communication.</li> <li>• Control of complex HW Chips .</li> </ul>

Note:

For each channel, the user configures the number of IB buffers (at least 1) if IB is selected for the current channel, or the maximum of data for EB buffers if EB is selected for the current channel.

### 7.2.2 LEVEL 0, Simple Synchronous behaviour

The intention of this functionality level is to provide a Handler/Driver with a reduced set of services to handle only simple synchronous transmissions. This is often the case for ECU including simple SPI networks but also for ECU using high speed external devices.

A simple synchronous transmission means that the function calling the transmission service is blocked during the ongoing transmission until the transmission is finished.

**[SWS\_Spi\_00160]** 「The LEVEL 0 SPI Handler/Driver shall offer a synchronous transfer service for SPI busses.」()

**[SWS\_Spi\_00161]** 「For an SPI Handler/Driver operating in LEVEL 0, when there is no on going Sequence transmission, the SPI Handler/Driver shall be in the idle state SPI\_IDLE.」()

**[SWS\_Spi\_00294]** 「This monolithic SPI Handler/Driver is able to handle one to n SPI buses according to the microcontroller used.」()

Then SPI buses are assigned to Jobs and not to Sequences. Consequently, Jobs, on different SPI buses, could belong to the same Sequence. Therefore:

**[SWS\_Spi\_00114]** 「The LEVEL 0 SPI Handler/Driver shall accept concurrent Spi\_SyncTransmit(), if the sequences to be transmitted use different bus and parameter SPI\_SUPPORT\_CONCURRENT\_SYNC\_TRANSMIT is enabled. This feature shall be disabled per default. That means during a Sequence on-going transmission, all requests to transmit another Sequence shall be rejected.」()

**[SWS\_Spi\_00115]** 「The LEVEL 0 SPI Handler/Driver behaviour shall include the common feature: Allowed Channel Buffers, which is selected.」()

**[SWS\_Spi\_00084]** 「If different Jobs (and consequently also Sequences) have common Channels, the SPI Handler/Driver' environment shall ensure that read and/or write functions are not called during transmission.」(SRS\_Spi\_12170)

**[SWS\_Spi\_00384]**「 When a hardware error is detected, the SPI Handler/Driver shall stop the current sequence, report an error to the DEM as configured and set the state of the Job to SPI\_JOB\_FAILED and the state of the Sequence to SPI\_SEQ\_FAILED.」()

Read and write functions can not guarantee the data integrity while Channel data is being transmitted.

### 7.2.3 LEVEL 1, Basic Asynchronous behavior

The intention of this functionality level is to provide a Handler/Driver with a reduced set of services to handle asynchronous transmissions only. This is often the case for ECU with functions related to SPI networks having different priorities but also for ECU using low speed external devices.

An asynchronous transmission means that the user calling the transmission service is not blocked when the transmission is on-going. Furthermore, the user can be notified at the end of transmission<sup>1</sup>.

**[SWS\_Spi\_00162]** 「The LEVEL 1 SPI Handler/Driver shall offer an asynchronous transfer service for SPI buses. An asynchronous transmission means that the user calling the transmission service is not blocked when the transmission is on-going.」(SRS\_Spi\_12099, SRS\_Spi\_12101, SRS\_Spi\_12103)

**[SWS\_Spi\_00295]** 「The LEVEL 1 SPI Handler/Driver shall offer an asynchronous transfer service for SPI buses. Furthermore, the user can be notified at the end of transmission.」()

**[SWS\_Spi\_00163]** 「For an SPI Handler/Driver operating in LEVEL 1, when there is no on-going Sequence transmission, the SPI Handler/Driver shall be in the idle state (SPI\_IDLE).」(SRS\_Spi\_12099, SRS\_Spi\_12101, SRS\_Spi\_12103)

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<sup>1</sup> This basic asynchronous behaviour might be implemented either by using interrupt or by polling mechanism. This software design choice is not in the scope of this document, but only solution is required for the LEVEL 1.

This Handler/Driver will be used by several software modules which may be independent from each other and also may belong to different layers. Therefore, priorities will be assigned to Jobs in order to figure out specific cases of multiple accesses. These cases usually occur within real time systems based on asynchronous mechanisms.

**[SWS\_Spi\_00002]** 「Jobs have priorities assigned. Jobs linked in a Sequence shall have same or de-creasing priorities. That means the first Job shall have the equal priority or the highest priority of all Jobs within the Sequence.」(SRS\_Spi\_12260)

**[SWS\_Spi\_00093]** 「Priority order of jobs shall be from the lower to the higher value defined, higher value higher priority (from 0, the lower to 3, the higher, limited to 4 priority levels see [\[SWS\\_Spi\\_00009\]](#)).」(SRS\_Spi\_12260, SRS\_Spi\_12150)

With reference to Jobs priorities, this Handler/Driver needs rules to make a decision in these specific cases of multiple accesses.

**[SWS\_Spi\_00059]** 「The SPI Handler/Driver scheduling method shall schedule Jobs in order to send the highest priority Job first.」(SRS\_Spi\_12260, SRS\_Spi\_12037)

This monolithic SPI Handler/Driver is able to handle one to n SPI busses according to the microcontroller used. But SPI busses are assigned to Jobs and not to Sequences. Consequently, Jobs on different SPI buses could belong to the same Sequence. Therefore:

**[SWS\_Spi\_00116]** 「The LEVEL 1 SPI Handler/Driver may allow transmitting more than one Sequence at the same time. That means during a Sequence transmission, all requests to transmit another Sequence shall be evaluated in order to accept to start a new sequence or to reject it accordingly to the lead Job.」()

**[SWS\_Spi\_00117]** 「The LEVEL 1 SPI Handler/Driver behaviour shall include the common feature: Allowed Channel Buffers, which is selected, and the configured asynchronous feature: Interruptible Sequence (see next chapter).」()

**[SWS\_Spi\_00267]** 「When a hardware error is detected, the SPI Handler/Driver shall stop the current Sequence, report an error to the DEM as configured and set the state of the Job to SPI\_JOB\_FAILED and the state of the Sequence to SPI\_SEQ\_FAILED.」()

**[SWS\_Spi\_00118]** 「If Jobs are configured with a specific end notification function, the SPI Handler/Driver shall call this notification function at the end of the Job transmission.」(SRS\_Spi\_12108)

**[SWS\_Spi\_00281]** 「If Sequences are configured with a specific end notification function, the SPI Handler/Driver shall call this notification function at the end of the Sequence transmission.」()

**[SWS\_Spi\_00119]** 「When a valid notification function pointer is configured (see [\[SWS\\_Spi\\_00071\]](#)), the SPI Handler/Driver shall call this notification function at the end of a Job transmission regardless of the result of the Job transmission being either `SPI_JOB_FAILED` or `SPI_JOB_OK` (rational: avoid deadlocks or endless loops).」(SRS\_Spi\_12108)

**[SWS\_Spi\_00120]** 「When a valid notification function pointer is configured (see [\[SWS\\_Spi\\_00073\]](#)), the SPI Handler/Driver shall call this notification function at the end of a Sequence transmission regardless of the result of the Sequence transmission being either `SPI_SEQ_FAILED`, `SPI_SEQ_OK` or `SPI_SEQ_CANCELLED` (rational: avoid deadlocks or endless loops).」(SRS\_Spi\_12108)

## 7.2.4 Asynchronous configurable feature: Interruptible Sequences

In order to allow taking advantages of asynchronous transmission mechanism, level 1 and level 2 of this SPI Handler/Driver have an optional feature with respect to suspending the transmission of Sequences.

Hence two main kinds of sequences can be used by configuration:

- Non-Interruptible Sequences, every Sequence transmission started is not suspended by the Handler/Driver until the end of transmission.
- Mixed Sequences, according to its configuration, a Sequence transmission started may be suspended by the Handler/Driver between two of their consecutive Jobs.

**[SWS\_Spi\_00121]** 「The SPI Handler/Driver's environment shall configure the `Spi-InterruptibleSeqAllowed` parameter (ON / OFF) in order to select which kind of Sequences the SPI Handler/Driver manages.」(SRS\_Spi\_13401)

### 7.2.4.1 Behavior of Non-Interruptible Sequences

The intention of the Non-Interruptible Sequences feature is to provide a simple software module based on a basic asynchronous mechanism, if only non blocking transmissions should be used.

**[SWS\_Spi\_00122]** 「Interruptible Sequences are not allowed within levels 1 and 2 of the SPI/Handler/Driver when the `SpiInterruptibleSeqAllowed` parameter is switched off (i.e. configured with value “OFF”).」(SRS\_Spi\_13401)

**[SWS\_Spi\_00123]** 「When the SPI Handler/Driver is configured not allowing interruptible Sequences, all Sequences declared are considered as Non-Interruptible Sequences<sup>2</sup>.」()

**[SWS\_Spi\_00282]** 「When the SPI Handler/Driver is configured not allowing interruptible Sequences their dedicated parameter `SpiInterruptibleSequence` can be omitted or the FALSE value should be used as default.」()

**[SWS\_Spi\_00124]** 「According to [\[SWS\\_Spi\\_00116\]](#) and [\[SWS\\_Spi\\_00122\]](#) requirements, the SPI Handler/Driver is not allowed to suspend a Sequence transmission already started in favour of another Sequence.」(SRS\_Spi\_12037)

#### 7.2.4.2 Behavior of Mixed Sequences

The intention of the Mixed Sequences feature is to provide a software module with specific asynchronous mechanisms, if, for instance, very long Sequences that could or should be suspended by others with higher priority are used.

**[SWS\_Spi\_00125]** 「Interruptible Sequences are allowed within levels 1 and 2 of SPI Handler/Driver when the `SpiInterruptibleSeqAllowed` parameter is switched on (i.e. configured with value “ON”).」(SRS\_Spi\_13401)

**[SWS\_Spi\_00126]** 「When the SPI Handler/Driver is configured allowing interruptible Sequences, all Sequences declared shall have their dedicated parameter `SpiInterruptibleSequence` (see [SWS\\_Spi\\_00064](#) & [SPI106](#)) to identify whether the Sequence can be suspended during transmission.」()

**[SWS\_Spi\_00014]** 「In case of a Sequence configured as Interruptible Sequence and according to [\[SWS\\_Spi\\_00125\]](#) requirement, the SPI Handler/Driver is allowed to suspend an already started Sequence transmission in favour of another Sequence with a higher priority Job (see [SWS\\_Spi\\_00002](#) & [SWS\\_Spi\\_00093](#)). That means, at the end of a Job transmission (that belongs to the interruptible sequence) with another Sequence transmit request pending, the SPI Handler/Driver shall perform a re-

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<sup>2</sup> The intention of this requirement is not to enforce any implementation solution in comparison with another one. But, it is only to ensure that anyhow, all Sequences will be considered as Non Interruptible Sequences.

scheduling in order to elect the next Job to transmit.」(SRS\_Spi\_12260, SRS\_Spi\_12037)

**[SWS\_Spi\_00127]** 「In case of a Sequence configured as Non-Interruptible Sequence and according to requirement [\[SWS\\_Spi\\_00125\]](#), the SPI Handler/Driver is not allowed to suspend this already started Sequence transmission in favour of another Sequence.」(SRS\_Spi\_12037)

**[SWS\_Spi\_00080]** 「When using Interruptible Sequences, the caller must be aware that if the multiple Sequences access the same Channels, the data for these Channels may be overwritten by the highest priority Job accessing each Channel.」()

### 7.2.5 LEVEL 2, Enhanced behaviour

The intention of this functionality level is to provide a Handler/Driver with a complete set of services to handle synchronous and asynchronous transmissions. This could be the case for ECU with a lot of functions related to SPI networks having different priorities but also for ECU using external devices with different speeds.

Handling asynchronous and synchronous transmissions means that the microcontroller for which this software module is dedicated has to provide more than one SPI bus (see [\[SWS\\_Spi\\_00108\]](#)). In fact, the goal is to support SPI buses using a so-called synchronous driver and to support other SPI buses using a so-called asynchronous driver.

**[SWS\_Spi\_00128]** 「The LEVEL 2 SPI Handler/Driver shall offer a synchronous transfer service for all SPI HW units configured as synchronous and it shall also offer an asynchronous transfer service for all other SPI buses.」()

**[SWS\_Spi\_00283]** 「In LEVEL 2 if there is no on going Sequence transmission, the SPI Handler/Driver shall be in idle state (SPI\_IDLE).」()

Usually, depending on software design, asynchronous end transmission may be detected by polling or interrupt mechanisms. This level of functionality proposes both mechanisms that are selectable during execution time.

**[SWS\_Spi\_00155]** 「The SPI Handler/Driver LEVEL 2 shall implement one polling mechanism mode and one interrupt mechanism mode for SPI busses handled asynchronously.」()

**[SWS\_Spi\_00156]** 「Both the polling mechanism and interrupt mechanism modes for SPI busses shall be selectable during execution time (see [\[SWS\\_Spi\\_00188\]](#)).」()

The requirements for LEVEL 0 apply to synchronous behaviour.  
The requirements for LEVEL 1 apply to asynchronous behaviour.

### 7.3 Scheduling Advices

For asynchronous levels, LEVEL 1 and LEVEL 2, the SPI Handler/Driver can call end notification functions at the end of a Job and/or Sequence transmission (see [\[SWS\\_Spi\\_00118\]](#)). In a second time, in case of interruptible Sequences (that could be suspended), if another Sequence transmit request is pending, a rescheduling is also done by the SPI Handler/Driver in order to elect the next Job to transmit (see [\[SWS\\_Spi\\_00014\]](#)).

**[SWS\_Spi\_00088]** 「For asynchronous levels, LEVEL 1 and LEVEL 2, the SPI Handler/Driver can call end notification functions at the end of a Job.」()

**[SWS\_Spi\_00268]** 「For asynchronous levels, LEVEL 1 and LEVEL 2, the SPI Handler/Driver can call end notification functions at the end of a Sequence transmission.」()

**[SWS\_Spi\_00269]** 「For asynchronous levels, LEVEL 1 and LEVEL 2 in case of interruptible Sequences, if another Sequence transmit request is pending, a rescheduling is also done by the SPI Handler/Driver in order to elect the next Job to transmit.」()

**[SWS\_Spi\_00270]** 「In case call end notification function and rescheduling are fully done by software, the order between these shall be first scheduling and then the call of end notification function executed.」()

**[SWS\_Spi\_00271]** 「In case call end notification function and rescheduling are fully done by hardware, the order could not be configured as required; the order shall be completely documented.」()

### 7.4 Error classification

The section 7.2 "Error Handling" of the document "General Specification of Basic Software Modules" describes the error handling of the Basic Software in detail. Above all, it constitutes a classification scheme consisting of five error types which may occur in BSW modules.

Based on this foundation, the following section specifies particular errors arranged in the respective subsections below.

### 7.4.1 Development Errors

#### [SWS\_Spi\_91001]

Type of error	Related error code	Error value
API service called with wrong parameter	SPI_E_PARAM_CHANNEL	0x0A
API service called with wrong parameter	SPI_E_PARAM_JOB	0x0B
API service called with wrong parameter	SPI_E_PARAM_SEQ	0x0C
API service called with wrong parameter	SPI_E_PARAM_LENGTH	0x0D
API service called with wrong parameter	SPI_E_PARAM_UNIT	0x0E
APIs called with a Null Pointer	SPI_E_PARAM_POINTER	0x10
API service used without module initialization	SPI_E_UNINIT	0x1A
API SPI_Init service called while the SPI driver has already been initialized	SPI_E_ALREADY_INITIALIZED	0x4A

()

### 7.4.2 Runtime Errors

#### [SWS\_Spi\_91002]

Type of error	Related error code	Error value
Services called in a wrong sequence	SPI_E_SEQ_PENDING	0x2A
Synchronous transmission service called at wrong time	SPI_E_SEQ_IN_PROCESS	0x3A

()

### 7.4.3 Transient faults

There are no transient faults.

### 7.4.4 Production Errors

There are no production errors.

### 7.4.5 Extended Production Errors

#### [SWS\_Spi\_00383]

<b>Error Name:</b>	SPI_E_HARDWARE_ERROR
<b>Short Description:</b>	An hardware error occurred during asynchronous or synchronous SPI

	transmit	
<b>Long Description:</b>	This Extended Production Error shall be issued when any error bit inside the SPI hardware transmit status register is raised	
<b>Detection Criteria:</b>	Fail	The SPI transmit status register information shall be reported to DEM as Dem_SetEventStatus (SPI_E_HARDWARE_ERROR, DEM_EVENT_STATUS_FAILED) when any error bit inside the SPI transmit status register is set. (SWS_Spi_00385)
	Pass	The SPI transmit status register information shall be reported to DEM as Dem_SetEventStatus (SPI_E_HARDWARE_ERROR, DEM_EVENT_STATUS_PASSED) when no error bit inside the SPI transmit status register is set. (SWS_Spi_00386)
<b>Secondary Parameters:</b>	N/A	
<b>Time Required:</b>	N/A	
<b>Monitor Frequency</b>	continuous	

」()

**[SWS\_Spi\_00385]**「 When any error bit inside the SPI transmit status register is set, the SPI transmit status register information shall be reported to DEM as Dem\_SetEventStatus (SPI\_E\_HARDWARE\_ERROR, DEM\_EVENT\_STATUS\_FAILED)」()

**[SWS\_Spi\_00386]**「 When no error bit inside the SPI transmit status register is set, the SPI transmit status register information shall be reported to DEM as Dem\_SetEventStatus (SPI\_E\_HARDWARE\_ERROR, DEM\_EVENT\_STATUS\_PASSED)」()

## 8 API specification

### 8.1 Imported types

In this chapter all types included from the following modules are listed:

[ ]

Module	Header File	Imported Type
Dem	Rte_Dem_Type.h	Dem_EventIdType
	Rte_Dem_Type.h	Dem_EventStatusType
Std	Std_Types.h	Std_ReturnType
	Std_Types.h	Std_VersionInfoType

]()

### 8.2 Type definitions

#### 8.2.1 Spi\_ConfigType

[SWS\_Spi\_00372]

<b>Name</b>	Spi_ConfigType	
<b>Kind</b>	Structure	
<b>Elements</b>	Implementation Specific	
	<b>Type</b>	--
	<b>Comment</b>	The contents of the initialization data structure are SPI specific.
<b>Description</b>	This type of the external data structure shall contain the initialization data for the SPI Handler/Driver.	
<b>Available via</b>	Spi.h	

]()

[SWS\_Spi\_00344] 「The description of the type Spi\_ConfigType is implementation specific and it shall be provided for external use.」()

#### 8.2.2 Spi\_StatusType

[SWS\_Spi\_00373]

<b>Name</b>	Spi_StatusType
<b>Kind</b>	Enumeration

<b>Range</b>	SPI_UNINIT	0x00	The SPI Handler/Driver is not initialized or not usable.
	SPI_IDLE	0x01	The SPI Handler/Driver is not currently transmitting any Job.
	SPI_BUSY	0x02	The SPI Handler/Driver is performing a SPI Job (transmit).
<b>Description</b>	This type defines a range of specific status for SPI Handler/Driver.		
<b>Available via</b>	Spi.h		

](SRS\_BSW\_00406, SRS\_BSW\_00335)

**[SWS\_Spi\_00061]** 「The type `Spi_StatusType` defines a range of specific status for SPI Handler/Driver. It informs about the SPI Handler/Driver status or specified SPI Hardware microcontroller peripheral.」(SRS\_BSW\_00335)

**[SWS\_Spi\_00259]** 「The type `Spi_StatusType` can be obtained calling the API service `Spi_GetStatus`.」()

**[SWS\_Spi\_00260]** 「The type `Spi_StatusType` can be obtained calling the API service `Spi_GetHWUnitStatus`.」()

**[SWS\_Spi\_00011]** 「After reset, the type `Spi_StatusType` shall have the default value `SPI_UNINIT`.」()

**[SWS\_Spi\_00345]** 「API service `Spi_GetStatus` shall return `SPI_UNINIT` when the SPI Handler/Driver is not initialized or not usable.」()

**[SWS\_Spi\_00346]** 「API service `Spi_GetStatus` shall return `SPI_IDLE` when The SPI Handler/Driver is not currently transmitting any Job.」()

**[SWS\_Spi\_00347]** 「API service `Spi_GetStatus` shall return `SPI_BUSY` when The SPI Handler/Driver is performing a SPI Job transmit.」()

**[SWS\_Spi\_00348]** 「`Spi_GetHWUnitStatus` function shall return `SPI_IDLE` when The SPI Hardware microcontroller peripheral is not currently transmitting any Job,」()

**[SWS\_Spi\_00349]** 「`Spi_GetHWUnitStatus` function shall return `SPI_BUSY` when The SPI Hardware microcontroller peripheral is performing a SPI Job transmit.」()

### 8.2.3 Spi\_JobResultType

**[SWS\_Spi\_00374]**「

<b>Name</b>	<code>Spi_JobResultType</code>
-------------	--------------------------------

Kind	Enumeration		
Range	SPI_JOB_OK	0x00	The last transmission of the Job has been finished successfully.
	SPI_JOB_PENDING	0x01	The SPI Handler/Driver is performing a SPI Job. The meaning of this status is equal to SPI_BUSY.
	SPI_JOB_FAILED	0x02	The last transmission of the Job has failed.
	SPI_JOB_QUEUED	0x03	An asynchronous transmit Job has been accepted, while actual transmission for this Job has not started yet.
Description	This type defines a range of specific Jobs status for SPI Handler/Driver.		
Available via	Spi.h		

]()

**[SWS\_Spi\_00062]** 「The type Spi\_JobResultType defines a range of specific Jobs status for SPI Handler/Driver.」(SRS\_BSW\_00335)

**[SWS\_Spi\_00261]** 「The type Spi\_JobResultType it informs about a SPI Handler/Driver Job status and can be obtained calling the API service Spi\_GetJobResult with the Job ID.」()

**[SWS\_Spi\_00012]** 「After reset, the type Spi\_JobResultType shall have the default value SPI\_JOB\_OK.」()

**[SWS\_Spi\_00350]** 「The function Spi\_GetJobResult shall return SPI\_JOB\_OK when the last transmission of the Job has been finished successfully.」()

## 8.2.4 Spi\_SeqResultType

**[SWS\_Spi\_00375]**「

Name	Spi_SeqResultType		
Kind	Enumeration		
Range	SPI_SEQ_OK	0x00	The last transmission of the Sequence has been finished successfully.
	SPI_SEQ_PENDING	0x01	The SPI Handler/Driver is performing a SPI Sequence. The meaning of this status is equal to SPI_BUSY.
	SPI_SEQ_FAILED	0x02	The last transmission of the Sequence has failed.
	SPI_SEQ_	0x03	The last transmission of the Sequence has been canceled

	CANCELED		by user.
<b>Description</b>	This type defines a range of specific Sequences status for SPI Handler/Driver.		
<b>Available via</b>	Spi.h		

]()

**[SWS\_Spi\_00351]** 「The type Spi\_SeqResultType defines a range of specific Sequences status for SPI Handler/Driver and can be obtained calling the API service Spi\_GetSequenceResult, it shall be provided for external use.」()

**[SWS\_Spi\_00019]** 「The type Spi\_SeqResultType defines the range of specific Sequences status for SPI Handler/Driver.」(SRS\_BSW\_00335)

**[SWS\_Spi\_00251]** 「The type Spi\_SeqResultType defines about SPI Handler/Driver Sequence status and can be obtained calling the API service Spi\_GetSequenceResult with the Sequence ID.」()

**[SWS\_Spi\_00017]** 「After reset, the type Spi\_SeqResultType shall have the default value SPI\_SEQ\_OK.」()

**[SWS\_Spi\_00352]** 「Spi\_GetSequenceResult function shall return SPI\_SEQ\_OK when the last transmission of the Sequence has been finished successfully.」()

**[SWS\_Spi\_00353]** 「Spi\_GetSequenceResult function shall return SPI\_SEQ\_PENDING when the SPI Handler/Driver is performing a SPI Sequence. The meaning of this status is equal to SPI\_BUSY.」()

**[SWS\_Spi\_00354]** 「Spi\_GetSequenceResult function shall return SPI\_SEQ\_FAILED when the last transmission of the Sequence has failed.」()

### 8.2.5 Spi\_DataBufferType

**[SWS\_Spi\_00376]**

<b>Name</b>	Spi_DataBufferType
<b>Kind</b>	Type
<b>Derived from</b>	uint8
<b>Description</b>	Type of application data buffer elements.

<b>Available via</b>	Spi.h
----------------------	-------

]()

**[SWS\_Spi\_00355]** 「Spi\_DataBufferType defines the type of application data buffer elements. Type is uint8. Access to the data is selected dynamically as is described in [SWS\\_SPI\\_00437](#). The data buffer has to be aligned to 32 bits. It shall be provided for external use.」()

**[SWS\_Spi\_00164]** 「The type Spi\_DataBufferType refers to application data buffer elements.」()

### 8.2.6 Spi\_NumberOfDataType

**[SWS\_Spi\_00377]**「

<b>Name</b>	Spi_NumberOfDataType
<b>Kind</b>	Type
<b>Derived from</b>	uint16
<b>Description</b>	Type for defining the number of data elements of the type Spi_DataBufferType to send and / or receive by Channel
<b>Available via</b>	Spi.h

]()

**[SWS\_Spi\_00165]** 「The type Spi\_NumberOfDataType is used for defining the number of data elements of the type specified in [SWS\\_SPI\\_00437](#) to send and / or receive by Channel.」()

### 8.2.7 Spi\_ChannelType

**[SWS\_Spi\_00378]**「

<b>Name</b>	Spi_ChannelType
<b>Kind</b>	Type
<b>Derived from</b>	uint8
<b>Description</b>	Specifies the identification (ID) for a Channel.
<b>Available via</b>	Spi.h

]()

**[SWS\_Spi\_00356]** 「The type `Spi_ChannelType` specifies the identification (ID) for a Channel.」()

**[SWS\_Spi\_00166]** 「The type `Spi_ChannelType` is used for specifying the identification (ID) for a Channel.」()

## 8.2.8 Spi\_JobType

**[SWS\_Spi\_00379]**「

<b>Name</b>	<code>Spi_JobType</code>
<b>Kind</b>	Type
<b>Derived from</b>	<code>uint16</code>
<b>Description</b>	Specifies the identification (ID) for a Job.
<b>Available via</b>	<code>Spi.h</code>

」()

**[SWS\_Spi\_00357]** 「The type `Spi_JobType` specifies the identification (ID) for a Job.」()

**[SWS\_Spi\_00167]** 「The type `Spi_JobType` is used for specifying the identification (ID) for a Job.」()

## 8.2.9 Spi\_SequenceType

**[SWS\_Spi\_00380]**「

<b>Name</b>	<code>Spi_SequenceType</code>
<b>Kind</b>	Type
<b>Derived from</b>	<code>uint8</code>
<b>Description</b>	Specifies the identification (ID) for a sequence of jobs.
<b>Available via</b>	<code>Spi.h</code>

」()

**[SWS\_Spi\_00358]** 「The type `Spi_SequenceType` specifies the identification (ID) for a sequence of jobs.」()

**[SWS\_Spi\_00168]** 「The type `Spi_SequenceType` is used for specifying the identification (ID) for a sequence of jobs.」()

### 8.2.10 Spi\_HWUnitType

[SWS\_Spi\_00381]

<b>Name</b>	Spi_HWUnitType
<b>Kind</b>	Type
<b>Derived from</b>	uint8
<b>Description</b>	Specifies the identification (ID) for a SPI Hardware microcontroller peripheral (unit).
<b>Available via</b>	Spi.h

]()

[SWS\_Spi\_00359] 「The type Spi\_HWUnitType specifies the identification (ID) for a SPI Hardware microcontroller peripheral (unit).」()

[SWS\_Spi\_00169] 「The type Spi\_HWUnitType is used for specifying the identification (ID) for a SPI Hardware microcontroller peripheral (unit).」()

### 8.2.11 Spi\_AsyncModeType

[SWS\_Spi\_00382]

<b>Name</b>	Spi_AsyncModeType		
<b>Kind</b>	Enumeration		
<b>Range</b>	SPI_POLLING_MODE	0x00	The asynchronous mechanism is ensured by polling, so interrupts related to SPI busses handled asynchronously are disabled.
	SPI_INTERRUPT_MODE	0x01	The asynchronous mechanism is ensured by interrupt, so interrupts related to SPI busses handled asynchronously are enabled.
<b>Description</b>	Specifies the asynchronous mechanism mode for SPI busses handled asynchronously in LEVEL 2.		
<b>Available via</b>	Spi.h		

]()

[SWS\_Spi\_00360] 「The type Spi\_AsyncModeType specifies the asynchronous mechanism mode for SPI busses handled asynchronously in LEVEL 2 and obtained by the API Spi\_SetAsyncMode.」()

[SWS\_Spi\_00170] 「The type Spi\_AsyncModeType is used for specifying the asynchronous mechanism mode for SPI busses handled asynchronously in LEVEL 2.」()

**[SWS\_Spi\_00150]** 「The type `Spi_AsyncModeType` is made available or not depending on the pre-compile time parameter: `SpiLevelDelivered`. This is only relevant for LEVEL 2.」()

**[SWS\_Spi\_00361]** 「If API `Spi_SetAsyncMode` function is called by the parameter value `SPI_POLLING_MODE` then asynchronous mechanism is ensured by polling. So interrupts related to SPI buses handled asynchronously are disabled.」()

**[SWS\_Spi\_00362]** 「If API `Spi_SetAsyncMode` function is called by the parameter value `SPI_INTERRUPT_MODE` asynchronous mechanism is ensured by interrupt, so interrupts related to SPI buses handled asynchronously are enabled.」()

## 8.3 Function definitions

### 8.3.1 Spi\_Init

**[SWS\_Spi\_00175]**

<b>Service Name</b>	Spi_Init	
<b>Syntax</b>	<pre>void Spi_Init (     const Spi_ConfigType* ConfigPtr )</pre>	
<b>Service ID [hex]</b>	0x00	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	ConfigPtr	Pointer to configuration set
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	None	
<b>Description</b>	Service for SPI initialization.	
<b>Available via</b>	Spi.h	

」()

**[SWS\_Spi\_00298]** 「The operation `Spi_Init` is Non Re-entrant.」()

**[SWS\_Spi\_00299]** 「The function `Spi_Init` provides the service for SPI initialization.」()

**[SWS\_Spi\_00013]** 「The function `Spi_Init` shall initialize all SPI relevant registers with the values of the structure referenced by the parameter `ConfigPtr`.」(SRS\_BSW\_00405, SRS\_BSW\_00101, SRS\_SPAL\_12057, SRS\_SPAL\_12125)

**[SWS\_Spi\_00015]** 「After the module initialization using the function `Spi_Init`, the SPI Handler/Driver shall set its state to `SPI_IDLE`, the Sequences result to `SPI_SEQ_OK` and the jobs result to `SPI_JOB_OK`.」(SRS\_BSW\_00406, SRS\_BSW\_00101, SRS\_SPAL\_12057)

**[SWS\_Spi\_00151]** 「For LEVEL 2 (see chapter 7.2.5 and [SPI103](#)), the function `Spi_Init` shall set the SPI Handler/Driver asynchronous mechanism mode to `SPI_POLLING_MODE` by default. Interrupts related to SPI busses shall be disabled.」()

A re-initialization of a SPI Handler/Driver by executing the `Spi_Init()` function requires a de-initialization before by executing a `Spi_DeInit()`.

Parameters of the function `Spi_Init` shall be checked as it is explained in section [API parameter checking](#)

### 8.3.2 Spi\_DeInit

**[SWS\_Spi\_00176]**

<b>Service Name</b>	Spi_DeInit	
<b>Syntax</b>	<pre>Std_ReturnType Spi_DeInit (     void )</pre>	
<b>Service ID [hex]</b>	0x01	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	None	
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Std_Return-	E_OK: de-initialisation command has been accepted

	Type	E_NOT_OK: de-initialisation command has not been accepted
<b>Description</b>	Service for SPI de-initialization.	
<b>Available via</b>	Spi.h	

]()

**[SWS\_Spi\_00300]** 「The operation Std\_ReturnType Spi\_DeInit( ) is Non Re-entrant.」()

**[SWS\_Spi\_00301]** 「When the API Spi\_DeInit has been accepted the return value of this function shall be E\_OK.」()

**[SWS\_Spi\_00302]** 「When the API Spi\_DeInit has not been accepted the return value of this function shall be E\_NOT\_OK.」()

**[SWS\_Spi\_00303]** 「The function Spi\_DeInit provides the service for SPI de-initialization.」()

**[SWS\_Spi\_00021]** 「The function Spi\_DeInit shall de-initialize SPI Handler/Driver.」(SRS\_BSW\_00336, SRS\_SPAL\_12163, SRS\_SPAL\_12064)

**[SWS\_Spi\_00252]** 「In case of the SPI Handler/Driver state is not SPI\_BUSY, the deInitialization function shall put all already initialized microcontroller SPI peripherals into the same state such as Power On Reset.」()

**[SWS\_Spi\_00253]** 「The function call Spi\_DeInit shall be rejected if the status of SPI Handler/Driver is SPI\_BUSY.」()

**[SWS\_Spi\_00022]** 「After the module de-initialization using the function Spi\_DeInit, the SPI Handler/Driver shall set its state to SPI\_UNINIT.」(SRS\_BSW\_00336, SRS\_SPAL\_12163)

The SPI Handler/Driver shall have been initialized before the function Spi\_DeInit is called, otherwise see [\[SWS\\_Spi\\_00046\]](#).

### 8.3.3 Spi\_WriteIB

**[SWS\_Spi\_00177]**「

<b>Service Name</b>	Spi_WriteIB	
<b>Syntax</b>	<pre>Std_ReturnType Spi_WriteIB (     Spi_ChannelType Channel,     const Spi_DataBufferType* DataBufferPtr )</pre>	
<b>Service ID [hex]</b>	0x02	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Channel	Channel ID.
	DataBufferPtr	Pointer to source data buffer. If this pointer is null, it is assumed that the data to be transmitted is not relevant and the default transmit value of this channel will be used instead.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Std_ReturnType	E_OK: write command has been accepted E_NOT_OK: write command has not been accepted
<b>Description</b>	Service for writing one or more data to an IB SPI Handler/Driver Channel specified by parameter.	
<b>Available via</b>	Spi.h	

⌋()

**[SWS\_Spi\_00304]** 「The operation Spi\_WriteIB is Re-entrant.⌋()

**[SWS\_Spi\_00305]** 「When the API Spi\_WriteIB command has been accepted the function returns the value E\_OK.⌋()

**[SWS\_Spi\_00306]** 「When the API Spi\_WriteIB command has not been accepted the function returns the value E\_NOT\_OK.⌋()

**[SWS\_Spi\_00307]** 「The function Spi\_WriteIB provides the service for writing one or more data to an IB SPI Handler/Driver Channel by the respective parameter.⌋()

**[SWS\_Spi\_00018]** 「The function Spi\_WriteIB shall write one or more data to an IB SPI Handler/Driver Channel specified by the respective parameter.⌋(SRS\_Spi\_12101, SRS\_Spi\_12153)

**[SWS\_Spi\_00024]** 「The function `Spi_WriteIB` shall take over the given parameters, and save the pointed data to the internal buffer defined with the function `Spi_Init.()`

**[SWS\_Spi\_00023]** 「If the given parameter “DataBufferPtr” is null, the function `Spi_WriteIB` shall assume that the data to be transmitted is not relevant and the default transmit value of the given channel shall be used instead.」()

**[SWS\_Spi\_00137]** 「The function `Spi_WriteIB` shall be pre-compile time configurable by the parameter `SpiChannelBuffersAllowed`. This function is only relevant for Channels with IB.」()

Parameters of the function `Spi_WriteIB` shall be checked as it is explained in section [API parameter checking](#).

The SPI Handler/Driver shall have been initialized before the function `Spi_WriteIB` is called, otherwise see [\[SWS\\_Spi\\_00046\]](#).

### 8.3.4 Spi\_AsyncTransmit

**[SWS\_Spi\_00178]**

<b>Service Name</b>	Spi_AsyncTransmit	
<b>Syntax</b>	<pre>Std_ReturnType Spi_AsyncTransmit (     Spi_SequenceType Sequence )</pre>	
<b>Service ID [hex]</b>	0x03	
<b>Sync/Async</b>	Asynchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Sequence	Sequence ID.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Std_Return-Type	E_OK: Transmission command has been accepted E_NOT_OK: Transmission command has not been accepted
<b>Description</b>	Service to transmit data on the SPI bus.	
<b>Available via</b>	Spi.h	

]()

**[SWS\_Spi\_00308]** 「The operation Std\_ReturnType Spi\_AsyncTransmit(Spi\_SequenceType Sequence) is Re-entrant.」()

**[SWS\_Spi\_00309]** 「When the API Spi\_AsyncTransmit command has been accepted the function shall return the value E\_OK.」()

**[SWS\_Spi\_00310]** 「When the API Spi\_AsyncTransmit command has not been accepted the function shall return the value E\_NOT\_OK.」()

**[SWS\_Spi\_00311]** 「The function Spi\_AsyncTransmit provides service to transmit data on the SPI bus.」()

**[SWS\_Spi\_00020]** 「The function Spi\_AsyncTransmit shall take over the given parameter, initiate a transmission, set the SPI Handler/Driver status to SPI\_BUSY, set the sequence result to SPI\_SEQ\_PENDING and return. 」(SRS\_Spi\_12099, SRS\_Spi\_12101, SRS\_Spi\_12103)

**[SWS\_Spi\_00194]** 「When the function Spi\_AsyncTransmit is called, shall take over the given parameter and set the Job status to SPI\_JOB\_QUEUED, which can be obtained by calling the API service Spi\_GetJobResult.」()

**[SWS\_Spi\_00157]** 「When the function Spi\_AsyncTransmit is called, the SPI Handler/Driver shall handle the Job results. Result shall be SPI\_JOB\_PENDING when the transmission of Jobs is started.」()

**[SWS\_Spi\_00292]** 「When the function Spi\_AsyncTransmit is called, the SPI Handler/Driver shall handle the Job results. Result shall be SPI\_JOB\_OK when the transmission of Jobs is success.」()

**[SWS\_Spi\_00293]** 「When the function Spi\_AsyncTransmit is called, the SPI Handler/Driver shall handle the Job results. Result shall be SPI\_JOB\_FAILED when the transmission of Jobs is failed.」()

**[SWS\_Spi\_00081]** 「When the function Spi\_AsyncTransmit is called and the requested Sequence is already in state SPI\_SEQ\_PENDING, the SPI Handler/Driver

shall not take in account this new request and this function shall return with value `E_NOT_OK`, in this case.」()

**[SWS\_Spi\_00266]** 「When the function `Spi_AsyncTransmit` is called and the requested Sequence is already in state `SPI_SEQ_PENDING` the SPI Handler/Driver shall report the `SPI_E_SEQ_PENDING` error according to [SWS\_BSW\_00042] and [SWS\_BSW\_00045].」()

**[SWS\_Spi\_00086]** 「When the function `Spi_AsyncTransmit` is called and the requested Sequence shares Jobs with another sequence that is in the state `SPI_SEQ_PENDING`, the SPI Handler/Driver shall not take into account this new request and this function shall return the value `E_NOT_OK`. In this case and according to [SWS\_BSW\_00042] and [SWS\_BSW\_00045], the SPI Handler/Driver shall report the `SPI_E_SEQ_PENDING` error.」()

**[SWS\_Spi\_00035]** 「When the function `Spi_AsyncTransmit` is used with EB and the source data pointer has been provided as `NULL` using the `Spi_SetupEB` method, the default transmit data configured for each channel shall be transmitted. (See also [\[SWS\\_SPI\\_00028\]](#))」(`SRS_Spi_12200`, `SRS_Spi_12201`)

**[SWS\_Spi\_00036]** 「When the function `Spi_AsyncTransmit` is used with EB and the destination data pointer has been provided as `NULL` using the `Spi_SetupEB` method, the SPI Handler/Driver shall ignore receiving data (See also [\[SWS\\_Spi\\_00030\]](#))」()

**[SWS\_Spi\_00055]** 「When the function `Spi_AsyncTransmit` is used for a Sequence with linked Jobs, the function shall transmit from the first Job up to the last Job in the sequence.」(`SRS_Spi_12181`)

**[SWS\_Spi\_00057]** 「At the end of a sequence transmission initiated by the function `Spi_AsyncTransmit` and if configured, the SPI Handler/Driver shall invoke the sequence notification call-back function after the last Job end notification if this one is also configured.」(`SRS_SPAL_00157`, `SRS_Spi_12108`)

**[SWS\_Spi\_00133]** 「The function `Spi_AsyncTransmit` is pre-compile time selectable by the configuration parameter `SpiLevelDelivered`. This function is only relevant for LEVEL 1 and LEVEL 2.」()

**[SWS\_Spi\_00173]** 「The SPI Handler/Driver's environment shall call the function `Spi_AsyncTransmit` after a function call of `Spi_SetupEB` for EB Channels or a

function call of `Spi_WriteIB` for IB Channels but before the function call `Spi_ReadIB.()`

Parameters of the function `Spi_AsyncTransmit` shall be checked as explained in section [API parameter checking](#)

The SPI Handler/Driver shall have been initialized before the function `Spi_AsyncTransmit` is called otherwise see [\[SWS\\_Spi\\_00046\]](#).

### 8.3.5 Spi\_ReadIB

**[SWS\_Spi\_00179]**

<b>Service Name</b>	Spi_ReadIB	
<b>Syntax</b>	<pre>Std_ReturnType Spi_ReadIB (     Spi_ChannelType Channel,     Spi_DataBufferType* DataBufferPointer )</pre>	
<b>Service ID [hex]</b>	0x04	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Channel	Channel ID.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	DataBufferPointer	Pointer to destination data buffer in RAM
<b>Return value</b>	Std_ReturnType	E_OK: read command has been accepted E_NOT_OK: read command has not been accepted
<b>Description</b>	Service for reading synchronously one or more data from an IB SPI Handler/Driver Channel specified by parameter.	
<b>Available via</b>	Spi.h	

`]`

**[SWS\_Spi\_00312]** 「The operation `Spi_ReadIB` is Re-entrant.」()

**[SWS\_Spi\_00313]** 「The function `Spi_ReadIB` return values E\_OK: read command has been accepted.」()

**[SWS\_Spi\_00314]** 「The function `Spi_ReadIB` return values E\_NOT\_OK: read command has not been accepted.」()

**[SWS\_Spi\_00315]** 「The function `Spi_ReadIB` provides the service for reading synchronously one or more data from an IB SPI Handler/Driver Channel specified by parameter.」()

**[SWS\_Spi\_00016]** 「The function `Spi_ReadIB` shall read synchronously one or more data from an IB SPI Handler/Driver Channel specified by the respective parameter.」(SRS\_Spi\_12099, SRS\_Spi\_12152)

**[SWS\_Spi\_00027]** 「The SPI Handler/Driver's environment shall call the function `Spi_ReadIB` after a Transmit method call to have relevant data within IB Channel.」()

**[SWS\_Spi\_00138]** 「The function `Spi_ReadIB` is pre-compile time configurable by the parameter `SpiChannelBuffersAllowed`. This function is only relevant for Channels with IB.」()

Parameters of the function `Spi_ReadIB` shall be checked as it is explained in section [API parameter checking](#).

The SPI Handler/Driver shall have been initialized before the function `Spi_ReadIB` is called otherwise see [\[SWS\\_Spi\\_00046\]](#).

### 8.3.6 Spi\_SetupEB

**[SWS\_Spi\_00180]**「

<b>Service Name</b>	Spi_SetupEB	
<b>Syntax</b>	<pre>Std_ReturnType Spi_SetupEB (     Spi_ChannelType Channel,     const Spi_DataBufferType* SrcDataBufferPtr,     Spi_DataBufferType* DesDataBufferPtr,     Spi_NumberOfDataType Length )</pre>	
<b>Service ID [hex]</b>	0x05	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Channel	Channel ID.
	SrcData BufferPtr	Pointer to source data buffer.
	Length	Length (number of data elements) of the data to be transmitted from SrcDataBufferPtr and/or received from DesDataBufferPtr Min.: 1 Max.:

		Max of data specified at configuration for this channel
<b>Parameters (inout)</b>	DesData BufferPtr	Pointer to destination data buffer in RAM.
<b>Parameters (out)</b>	None	
<b>Return value</b>	Std_-Return-Type	E_OK: Setup command has been accepted E_NOT_OK: Setup command has not been accepted
<b>Description</b>	Service to setup the buffers and the length of data for the EB SPI Handler/Driver Channel specified.	
<b>Available via</b>	Spi.h	

]()

**[SWS\_Spi\_00316]** 「The operation Spi\_SetupEB is Re-entrant.」()

**[SWS\_Spi\_00317]** 「Return values of the function Spi\_SetupEB are E\_OK: Setup command has been accepted and E\_NOT\_OK: Setup command has not been accepted.」()

**[SWS\_Spi\_00318]** 「The function Spi\_SetupEB provides the service to setup the buffers and the length of data for the EB SPI Handler/Driver Channel specified.」()

**[SWS\_Spi\_00058]** 「The function Spi\_SetupEB shall set up the buffers and the length of data for the specific EB SPI Handler/Driver Channel.」(SRS\_Spi\_12103)

**[SWS\_Spi\_00067]** 「The function Spi\_SetupEB shall update the buffer pointers and length attributes of the specified Channel with the provided values.」(SRS\_Spi\_12103)

As these attributes are persistent, they will be used for all succeeding calls to a Transmit method (for the specified Channel).

**[SWS\_Spi\_00028]** 「When the SPI Handler/Driver's environment is calling the function Spi\_SetupEB with the parameter SrcDataBufferPtr being a Null pointer, the function shall transmit the default transmit value configured for the channel after a Transmit method is requested. (See also [\[SWS\\_Spi\\_00035\]](#))」()

**[SWS\_Spi\_00030]** 「When the function `Spi_SetupEB` is called with the parameter `DesDataBufferPtr` being a Null pointer, the SPI Handler/Driver shall ignore the received data after a Transmit method is requested.(See also [\[SWS\\_Spi\\_00036\]](#))」()

**[SWS\_Spi\_00037]** 「The SPI Handler/Driver's environment shall call the `Spi_SetupEB` function once for each Channel with EB declared before the SPI Handler/Driver's environment calls a Transmit method on them.」()

**[SWS\_Spi\_00139]** 「The function `Spi_SetupEB` is pre-compile time configurable by the parameter `SpiChannelBuffersAllowed`. This function is only relevant for Channels with EB.」()

Parameters of the function `Spi_SetupEB` shall be checked as it is explained in section API parameter checking.

The SPI Handler/Driver shall have been initialized before the function `Spi_SetupEB` is called otherwise see [\[SWS\\_Spi\\_00046\]](#).

### 8.3.7 Spi\_GetStatus

**[SWS\_Spi\_00181]**

<b>Service Name</b>	Spi_GetStatus	
<b>Syntax</b>	<pre>Spi_StatusType Spi_GetStatus (     void )</pre>	
<b>Service ID [hex]</b>	0x06	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	None	
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Spi_StatusType	Spi_StatusType
<b>Description</b>	Service returns the SPI Handler/Driver software module status.	
<b>Available via</b>	Spi.h	

]()

**[SWS\_Spi\_00319]** 「The operation `Spi_GetStatus` is Re-entrant.」()

**[SWS\_Spi\_00320]** 「The function `Spi_GetStatus` returns the SPI Handler/Driver software module status.」()

**[SWS\_Spi\_00025]** 「The function `Spi_GetStatus` shall return the SPI Handler/Driver software module status.」(SRS\_SPAL\_12064, SRS\_Spi\_12104)

### 8.3.8 Spi\_GetJobResult

**[SWS\_Spi\_00182]**

<b>Service Name</b>	Spi_GetJobResult	
<b>Syntax</b>	<pre>Spi_JobResultType Spi_GetJobResult (     Spi_JobType Job )</pre>	

<b>Service ID [hex]</b>	0x07	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Job	Job ID. An invalid job ID will return an undefined result.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Spi_JobResultType	Spi_JobResultType
<b>Description</b>	This service returns the last transmission result of the specified Job.	
<b>Available via</b>	Spi.h	

]()

**[SWS\_Spi\_00321]** 「The operation `Spi_GetJobResult` is Re-entrant.」()

**[SWS\_Spi\_00322]** 「The function `Spi_GetJobResult` service returns the last transmission result of the specified Job.」()

**[SWS\_Spi\_00026]** 「The function `Spi_GetJobResult` shall return the last transmission result of the specified Job. 」(SRS\_SPAL\_00157, SRS\_Spi\_12104)

**[SWS\_Spi\_00038]** 「The SPI Handler/Driver's environment shall call the function `Spi_GetJobResult` to inquire whether the Job transmission has succeeded (`SPI_JOB_OK`) or failed (`SPI_JOB_FAILED`).」(SRS\_SPAL\_00157)

NOTE: Every new transmit job that has been accepted by the SPI Handler/Driver overwrites the previous job result with `SPI_JOB_QUEUED` or `SPI_JOB_PENDING`.

Parameters of the function `Spi_GetJobResult` shall be checked as it is explained in section API parameter checking.

If SPI Handler/Driver has not been initialized before the function `Spi_GetJobResult` is called, the return value is undefined.

### 8.3.9 Spi\_GetSequenceResult

**[SWS\_Spi\_00183]**「

<b>Service Name</b>	Spi_GetSequenceResult
<b>Syntax</b>	Spi_SeqResultType Spi_GetSequenceResult (

	Spi_SequenceType Sequence )	
<b>Service ID [hex]</b>	0x08	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Sequence	Sequence ID. An invalid sequence ID will return an undefined result.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Spi_SeqResult-Type	Spi_SeqResultType
<b>Description</b>	This service returns the last transmission result of the specified Sequence.	
<b>Available via</b>	Spi.h	

]()

**[SWS\_Spi\_00323]** 「The operation Spi\_GetSequenceResult is Re-entrant.」()

**[SWS\_Spi\_00324]** 「The function Spi\_GetSequenceResult shall return the last transmission result of the specified Sequence.」( SRS\_SPAL\_00157, SRS\_Spi\_12104)

**[SWS\_Spi\_00042]** 「The SPI Handler/Driver's environment shall call the function Spi\_GetSequenceResult to inquire whether the full Sequence transmission has succeeded (SPI\_SEQ\_OK) or failed (SPI\_SEQ\_FAILED).」(SRS\_SPAL\_00157, SRS\_Spi\_12170)

Note:

- Every new transmit sequence that has been accepted by the SPI Handler/Driver overwrites the previous sequence result with SPI\_SEQ\_PENDING.
- If the SPI Handler/Driver has not been initialized before the function Spi\_GetSequenceResult is called, the return value is undefined.

Parameters of the function Spi\_GetSequenceResult shall be checked as it is explained in section [API parameter checking](#).

### 8.3.10 Spi\_GetVersionInfo

**[SWS\_Spi\_00184]**「

<b>Service Name</b>	Spi_GetVersionInfo	
<b>Syntax</b>	<pre>void Spi_GetVersionInfo (     Std_VersionInfoType* versioninfo )</pre>	
<b>Service ID [hex]</b>	0x09	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	None	
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	versioninfo	Pointer to where to store the version information of this module.
<b>Return value</b>	None	
<b>Description</b>	This service returns the version information of this module.	
<b>Available via</b>	Spi.h	

]()

**[SWS\_Spi\_00371]** 「If Det is enabled, the parameter versioninfo shall be checked for being NULL. The error SPI\_E\_PARAM\_POINTER shall be reported in case the value is a NULL pointer.」()

### 8.3.11 Spi\_SyncTransmit

**[SWS\_Spi\_00185]**

<b>Service Name</b>	Spi_SyncTransmit	
<b>Syntax</b>	<pre>Std_ReturnType Spi_SyncTransmit (     Spi_SequenceType Sequence )</pre>	
<b>Service ID [hex]</b>	0x0a	
<b>Sync/Async</b>	Asynchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Sequence	Sequence ID.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Std_Return-Type	E_OK: Transmission command has been accepted E_NOT_OK: Transmission command has not been accepted

<b>Description</b>	Service to transmit data on the SPI bus
<b>Available via</b>	Spi.h

]()

**[SWS\_Spi\_00327]** 「The operation Spi\_SyncTransmit is Re-entrant.」()

**[SWS\_Spi\_00328]** 「Return value of the function Spi\_SyncTransmit is E\_OK: when Transmission command has been accepted.」()

**[SWS\_Spi\_00329]** 「Return value of the function Spi\_SyncTransmit is E\_NOT\_OK: When Transmission command has not been accepted.」()

**[SWS\_Spi\_00330]** 「The function Spi\_SyncTransmit provides the service to transmit data on the SPI bus.」()

**[SWS\_Spi\_00134]** 「When the function Spi\_SyncTransmit is called, shall take over the given parameter and set the SPI Handler/Driver status to SPI\_BUSY can be obtained calling the API service SPI\_GetStatus.」(SRS\_Spi\_12152, SRS\_Spi\_12153, SRS\_Spi\_12154)

**[SWS\_Spi\_00285]** 「When the function Spi\_SyncTransmit is called, shall take over the given parameter and set the Sequence status to SPI\_SEQ\_PENDING can be obtained calling the API service Spi\_GetSequenceResult.」()

**[SWS\_Spi\_00286]** 「When the function Spi\_SyncTransmit is called, shall take over the given parameter and set the Job status to SPI\_JOB\_PENDING can be obtained calling the API service Spi\_GetJobResult.」()

**[SWS\_Spi\_00135]** 「When the function Spi\_SyncTransmit is called while a sequence is on transmission and SPI\_SUPPORT\_CONCURRENT\_SYNC\_TRANSMIT is disabled or another sequence is on transmission on same bus, the SPI Handler/Driver shall not take into account this new transmission request and the function shall return the value E\_NOT\_OK (see [\[SWS\\_Spi\\_00114\]](#)). In this case, the SPI Handler/Driver shall report the SPI\_E\_SEQ\_IN\_PROCESS error according to [\[SWS\\_BSW\\_00042\]](#) and [\[SWS\\_BSW\\_00045\]](#).」(SRS\_Spi\_12093)

**[SWS\_Spi\_00136]** 「The function `Spi_SyncTransmit` is pre-compile time selectable by the configuration parameter `SpiLevelDelivered`. This function is only relevant for LEVEL 0 and LEVEL 2.」()

Parameters of the function `Spi_SyncTransmit` shall be checked as it is explained in section API parameter checking

### 8.3.12 Spi\_GetHWUnitStatus

**[SWS\_Spi\_00186]**

<b>Service Name</b>	Spi_GetHWUnitStatus	
<b>Syntax</b>	<pre>Spi_StatusType Spi_GetHWUnitStatus (     Spi_HWUnitType HWUnit )</pre>	
<b>Service ID [hex]</b>	0x0b	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	HWUnit	SPI Hardware microcontroller peripheral (unit) ID.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Spi_StatusType	Spi_StatusType
<b>Description</b>	This service returns the status of the specified SPI Hardware microcontroller peripheral.	
<b>Available via</b>	Spi.h	

」()

**[SWS\_Spi\_00331]** 「The operation `Spi_GetHWUnitStatus` is Re-entrant.」()

**[SWS\_Spi\_00332]** 「The function `Spi_GetHWUnitStatus` service returns the status of the specified SPI Hardware microcontroller peripheral.」()

**[SWS\_Spi\_00141]** 「The function `Spi_GetHWUnitStatus` shall return the status of the specified SPI Hardware microcontroller peripheral.」()

**[SWS\_Spi\_00287]** 「The SPI Handler/Driver's environment shall call this function to inquire whether the specified SPI Hardware microcontroller peripheral is SPI\_IDLE or SPI\_BUSY.」()

**[SWS\_Spi\_00142]** 「The function `Spi_GetHWUnitStatus` is pre-compile time configurable On / Off by the configuration parameter `SpiHwStatusApi.`」()

Parameters of the function `Spi_GetHWUnitStatus` shall be checked as it is explained in section [API parameter checking](#).

If SPI Handler/Driver has not been initialized before the function `Spi_GetHWUnitStatus` is called, the return value is undefined.

### 8.3.13 Spi\_Cancel

**[SWS\_Spi\_00187]**「

<b>Service Name</b>	Spi_Cancel	
<b>Syntax</b>	<pre>void Spi_Cancel (     Spi_SequenceType Sequence )</pre>	
<b>Service ID [hex]</b>	0x0c	
<b>Sync/Async</b>	Asynchronous	
<b>Reentrancy</b>	Reentrant	
<b>Parameters (in)</b>	Sequence	Sequence ID.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	None	
<b>Description</b>	Service cancels the specified on-going sequence transmission.	
<b>Available via</b>	Spi.h	

」()

**[SWS\_Spi\_00333]** 「The operation `Spi_Cancel` is Re-entrant.」()

**[SWS\_Spi\_00334]** 「The function `Spi_Cancel` service cancels the specified on-going sequence transmission.」()

**[SWS\_Spi\_00144]** 「The function `Spi_Cancel` shall cancel the specified on-going sequence transmission without cancelling any Job transmission and set the sequence result to `SPI_SEQ_CANCELLED.`」()

With other words, the `Spi_Cancel` function stops a Sequence transmission after a (possible) on transmission Job ended and before a (potential) next Job transmission starts.

**[SWS\_Spi\_00145]** 「When the sequence is cancelled by the function `Spi_Cancel` and if configured, the SPI Handler/Driver shall call the sequence notification call-back function instead of starting a potential next job belonging to it.」()

**[SWS\_Spi\_00146]** 「The function `Spi_Cancel` is pre-compile time configurable On / Off by the configuration parameter `SpiCancelApi.`」()

The SPI Handler/Driver is not responsible on external devices damages or undefined state due to cancelling a sequence transmission. It is up to the SPI Handler/Driver's environment to be aware to what it is doing!

### 8.3.14 Spi\_SetAsyncMode

**[SWS\_Spi\_00188]**

<b>Service Name</b>	Spi_SetAsyncMode	
<b>Syntax</b>	<pre>Std_ReturnType Spi_SetAsyncMode (     Spi_AsyncModeType Mode )</pre>	
<b>Service ID [hex]</b>	0x0d	
<b>Sync/Async</b>	Synchronous	
<b>Reentrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	Mode	New mode required.
<b>Parameters (inout)</b>	None	
<b>Parameters (out)</b>	None	
<b>Return value</b>	Std_ReturnType	E_OK: Setting command has been done E_NOT_OK: setting command has not been accepted
<b>Description</b>	Service to set the asynchronous mechanism mode for SPI busses handled asynchronously.	
<b>Available via</b>	Spi.h	

()

**[SWS\_Spi\_00335]** 「The operation `Spi_SetAsyncMode` is Non Re-entrant.」()

**[SWS\_Spi\_00336]** 「Return value of the function `Spi_SetAsyncMode` is `E_OK`: Setting command has been done.」()

**[SWS\_Spi\_00337]** 「Return value of the function `Spi_SetAsyncMode` is `E_NOT_OK`: setting command has not been accepted.」()

**[SWS\_Spi\_00338]** 「The function `Spi_SetAsyncMode` service to set the asynchronous mechanism mode for SPI buses handled asynchronously.」()

**[SWS\_Spi\_00152]** 「The function `Spi_SetAsyncMode` according to the given parameter shall set the asynchronous mechanism mode for SPI channels configured to behave asynchronously.」()

**[SWS\_Spi\_00171]** 「If the function `Spi_SetAsyncMode` is called while the SPI Handler/Driver status is `SPI_BUSY` and an asynchronous transmission is in progress, the SPI Handler/Driver shall not change the `AsyncModeType` and keep the mode type as it is. The function shall return the value `E_NOT_OK`.」()

**[SWS\_Spi\_00172]** 「If `Spi_SetAsyncMode` is called while a synchronous transmission is in progress, the SPI Handler/Driver shall set the `AsyncModeType` according to parameter 'Mode', even if the SPI Handler/Driver status is `SPI_BUSY`. The function shall return the value `E_OK`.」()

**[SWS\_Spi\_00154]** 「The function `Spi_SetAsyncMode` is pre-compile time selectable by the configuration parameter `SpiLevelDelivered`. This function is only relevant for LEVEL 2.」()

## 8.4 Callback notifications

This chapter lists all functions provided by the SPI module to lower layer modules.

The SPI Handler/Driver module belongs to the lowest layer of AUTOSAR Software Architecture hence this module specification has not identified any callback functions.

## 8.5 Scheduled functions

This chapter lists all functions provided by the SPI Handler/Driver and called directly by the Basic Software Module Scheduler.

The SPI Handler/Driver module requires a scheduled function for the management of the asynchronous mode managed with polling (see [SWS\\_Spi\\_00361](#)). The specified functions below exemplify how to implement them if they are needed.

### 8.5.1 Spi\_MainFunction\_Handling

[SWS\_Spi\_00189]

<b>Service Name</b>	Spi_MainFunction_Handling
<b>Syntax</b>	<pre>void Spi_MainFunction_Handling (     void )</pre>
<b>Service ID [hex]</b>	0x10
<b>Description</b>	--
<b>Available via</b>	SchM_Spi.h

()

This function shall poll the SPI interrupts linked to HW Units allocated to the transmission of SPI sequences to enable the evolution of transmission state machine.

## 8.6 Expected Interfaces

This chapter lists all functions that the SPI Handler/Driver requires from other modules.

### 8.6.1 Mandatory Interfaces

The SPI Handler/Driver module requires some interfaces to fulfill its core functionality.

[SWS\_Spi\_00389]

<b>API Function</b>	<b>Header File</b>	<b>Description</b>
Det_Report- RuntimeError	Det.h	Service to report runtime errors. If a callout has been configured then this callout shall be called.

()

### 8.6.2 Optional Interfaces

This chapter defines all interfaces which are required to fulfill an optional functionality of SPI Handler/Driver module.

#### [SWS\_Spi\_00339]

API Function	Header File	Description
Dem_Set-EventStatus	Dem.h	Called by SW-Cs or BSW modules to report monitor status information to the Dem. BSW modules calling Dem_SetEventStatus can safely ignore the return value. This API will be available only if $\{\{\text{Dem/DemConfigSet/DemEventParameter/DemEventReportingType}\} == \text{STANDARD\_REPORTING}\}$
Det - ReportError	Det.h	Service to report development errors.

]()

### 8.6.3 Configurable interfaces

In this chapter all interfaces are listed where the target function could be configured. The target function is usually a call-back function. The name of these interfaces is not fixed because they are configurable.

**[SWS\_Spi\_00075]** 「The SPI Handler/Driver shall use the callback routines Spi\_JobEndNotification to inform other software modules about certain states or state changes.」(SRS\_SPAL\_00157)

**[SWS\_Spi\_00264]** 「The SPI Handler/Driver shall use the callback routines Spi\_SeqEndNotification to inform other software modules about certain states or state changes.」()

**[SWS\_Spi\_00265]** 「For implement the call back function other modules are required to provide the routines in the expected manner.」()

**[SWS\_Spi\_00044]** 「The SPI Handler/Driver's implementer must implement the callback notifications Spi\_JobEndNotification and Spi\_SeqEndNotification as function pointers defined within the initialization data structure (Spi\_ConfigType).」(SRS\_SPAL\_12056)

**[SWS\_Spi\_00048]** 「The callback notifications Spi\_JobEndNotification and Spi\_SeqEndNotification shall have no parameters and no return value.」(SRS\_BSW\_00359, SRS\_BSW\_00360, SRS\_BSW\_00369)

**[SWS\_Spi\_00054]** 「If a callback notification is configured as null pointer, no callback shall be executed.」(SRS\_SPAL\_12056)

**[SWS\_Spi\_00085]** 「It is allowed to use the following API calls within the SPI callback notifications:

- Spi\_ReadIB
- Spi\_WriteIB
- Spi\_SetupEB
- Spi\_GetJobResult
- Spi\_GetSequenceResult
- Spi\_GetHWUnitStatus
- Spi\_Cancel

All other SPI Handler/Driver API calls are not allowed.」()

### 8.6.3.1 Spi\_JobEndNotification

**[SWS\_Spi\_00192]**「

<b>Service Name</b>	(*Spi_JobEndNotification)
<b>Syntax</b>	void (*Spi_JobEndNotification) ( void )
<b>Sync/Async</b>	Synchronous
<b>Reentrancy</b>	Reentrant
<b>Parameters (in)</b>	None
<b>Parameters (inout)</b>	None
<b>Parameters (out)</b>	None
<b>Return value</b>	None
<b>Description</b>	Callback routine provided by the user for each Job to notify the caller that a job has been finished.
<b>Available via</b>	Spi_Externals.h

」()

**[SWS\_Spi\_00340]** 「The operation SpiJobEndNotification is Re-entrant.」()

**[SWS\_Spi\_00071]** 「If the SpiJobEndNotification is configured (i.e. not a null pointer), the SPI Handler/Driver shall call the configured callback notification at the end of a Job transmission.」(SRS\_SPAL\_00157)

Note: This routine might be called on interrupt level, depending on the calling function.

### 8.6.3.2 Spi\_SeqEndNotification

#### [SWS\_Spi\_00193]

<b>Service Name</b>	(*Spi_SeqEndNotification)
<b>Syntax</b>	void (*Spi_SeqEndNotification) ( void )
<b>Sync/Async</b>	Synchronous
<b>Reentrancy</b>	Reentrant
<b>Parameters (in)</b>	None
<b>Parameters (inout)</b>	None
<b>Parameters (out)</b>	None
<b>Return value</b>	None
<b>Description</b>	Callback routine provided by the user for each Sequence to notify the caller that a sequence has been finished.
<b>Available via</b>	Spi_Externals.h

]()

[SWS\_Spi\_00341] 「The operation SpiSeqEndNotification is Re-entrant.」()

[SWS\_Spi\_00073] 「If the SpiSeqEndNotification is configured (i.e. not a null pointer), the SPI Handler/Driver shall call the configured callback notification at the end of a Sequence transmission.」(SRS\_SPAL\_00157)

Note: This routine might be called on interrupt level, depending on the calling function.

## 8.7 Error detection

### 8.7.1 API parameter checking

**[SWS\_Spi\_00004]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_PARAM\_CHANNEL when API service called with wrong parameter.」(SRS\_BSW\_00327, SRS\_BSW\_00337, SRS\_BSW\_00385)

**[SWS\_Spi\_00237]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_PARAM\_JOB when API service called with wrong parameter.」()

**[SWS\_Spi\_00238]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_PARAM\_SEQ when API service called with wrong parameter.」()

**[SWS\_Spi\_00240]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_PARAM\_LENGTH when API service called with wrong parameter.」()

**[SWS\_Spi\_00241]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_PARAM\_UNIT when API service called with wrong parameter.」()

**[SWS\_Spi\_00031]** 「The API parameter `Channel` shall have a value within the defined channels in the initialization data structure, and the correct type of channel (IB or EB) has to be used with services. Related error value: SPI\_E\_PARAM\_CHANNEL. Otherwise, the service is not done and the return value shall be E\_NOT\_OK.」(SRS\_BSW\_00323)

**[SWS\_Spi\_00032]** 「The API parameters `Sequence` and `Job` shall have values within the specified range of values. Related errors values: SPI\_E\_PARAM\_SEQ or SPI\_E\_PARAM\_JOB.」(SRS\_BSW\_00323)

**[SWS\_Spi\_00060]** 「The API parameter `Length of data` shall have a value within the specified buffer maximum value. Related error value: SPI\_E\_PARAM\_LENGTH.」(SRS\_BSW\_00323)

**[SWS\_Spi\_00258]** 「If the API parameter `Length` related service is not done and the return value shall be E\_NOT\_OK.」()

**[SWS\_Spi\_00143]** 「The API parameter `HWUnit` shall have a value within the specified range of values. Related error value: SPI\_E\_PARAM\_UNIT.」()

**[SWS\_Spi\_00288]** 「If `HWUnit` related service is not done and the return value shall be SPI\_UNINIT.」()

**[SWS\_Spi\_00235]** 「If not applicable, the SPI Handler/Driver module's environment shall pass a NULL pointer to the function Spi\_Init.」()

### 8.7.2 SPI state checking

**[SWS\_Spi\_00242]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_UNINIT when API service used without module initialization.」()

**[SWS\_Spi\_00046]** 「If development error detection for the SPI module is enabled and the SPI Handler/Driver's environment calls any API function before initialization, an error should be reported to the DET with the error value SPI\_E\_UNINIT according to the configuration.」(SRS\_BSW\_00406)

**[SWS\_Spi\_00246]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_ALREADY\_INITIALIZED when API SPI\_Init service called while the SPI driver has already been initialized time.」()

**[SWS\_Spi\_00233]** 「  
If development error detection for the SPI module is enabled, the calling of the routine SPI\_Init() while the SPI driver is already initialized will cause a development error SPI\_E\_ALREADY\_INITIALIZED and the desired functionality shall be left without any action.」()

### 8.7.3 SPI runtime checking

**[SWS\_Spi\_00243]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_SEQ\_PENDING when services called in a wrong sequence.」()

**[SWS\_Spi\_00245]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_SEQ\_IN\_PROCESS when synchronous transmission service called at wrong time.」()

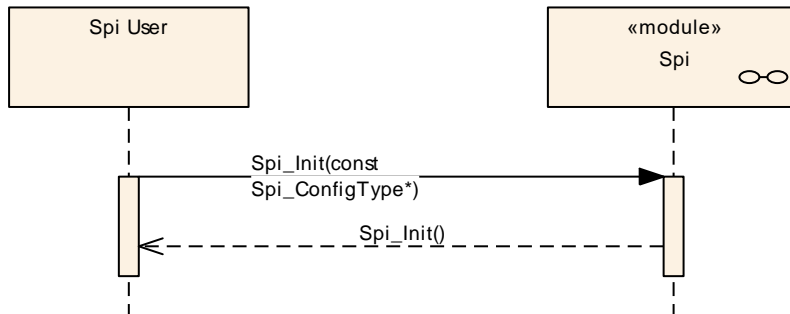
**[SWS\_Spi\_00195]** 「SPI Handler/driver shall be able to detect the error SPI\_E\_HARDWARE\_ERROR when an hardware error occur during asynchronous or synchronous transmit. Please see also SWS\_Spi\_00267 and SWS\_Spi\_00384.」()

**[SWS\_Spi\_00254]** 「If the Sequence and Job related service is not done and, depending on services, either the return value shall be E\_NOT\_OK or a failed result (SPI\_JOB\_FAILED or SPI\_SEQ\_FAILED).」()

**[SWS\_Spi\_00256]** 「The SPI Handler/Driver shall not process the invoked function but, depending on the invoked function, shall either return the value E\_NOT\_OK or a failed result (SPI\_JOB\_FAILED or SPI\_SEQ\_FAILED).」()

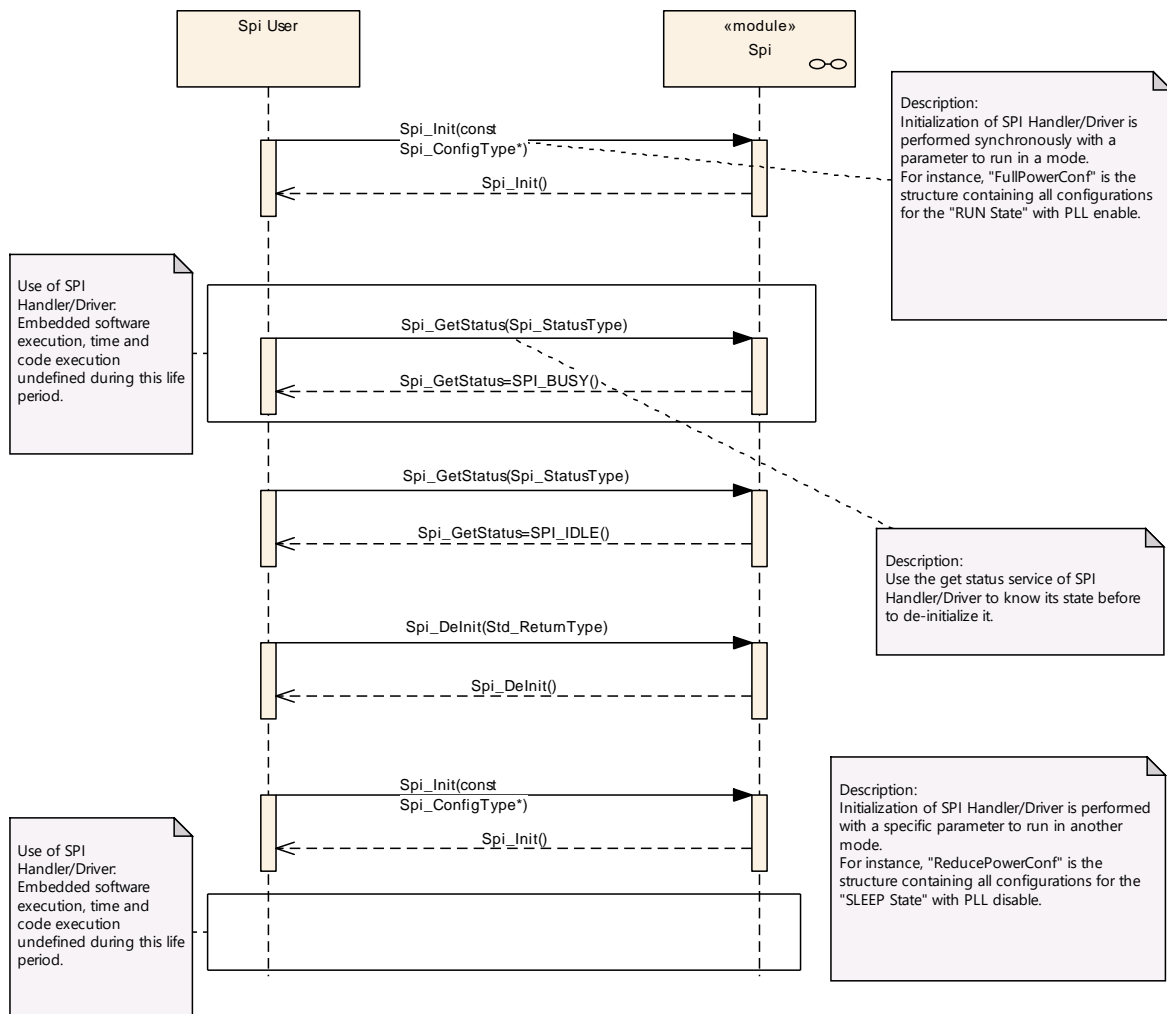
## 9 Sequence diagrams

### 9.1 Initialization



### 9.2 Modes transitions

The following sequence diagram shows an example of an Init / Delnit calls for a running mode transition.



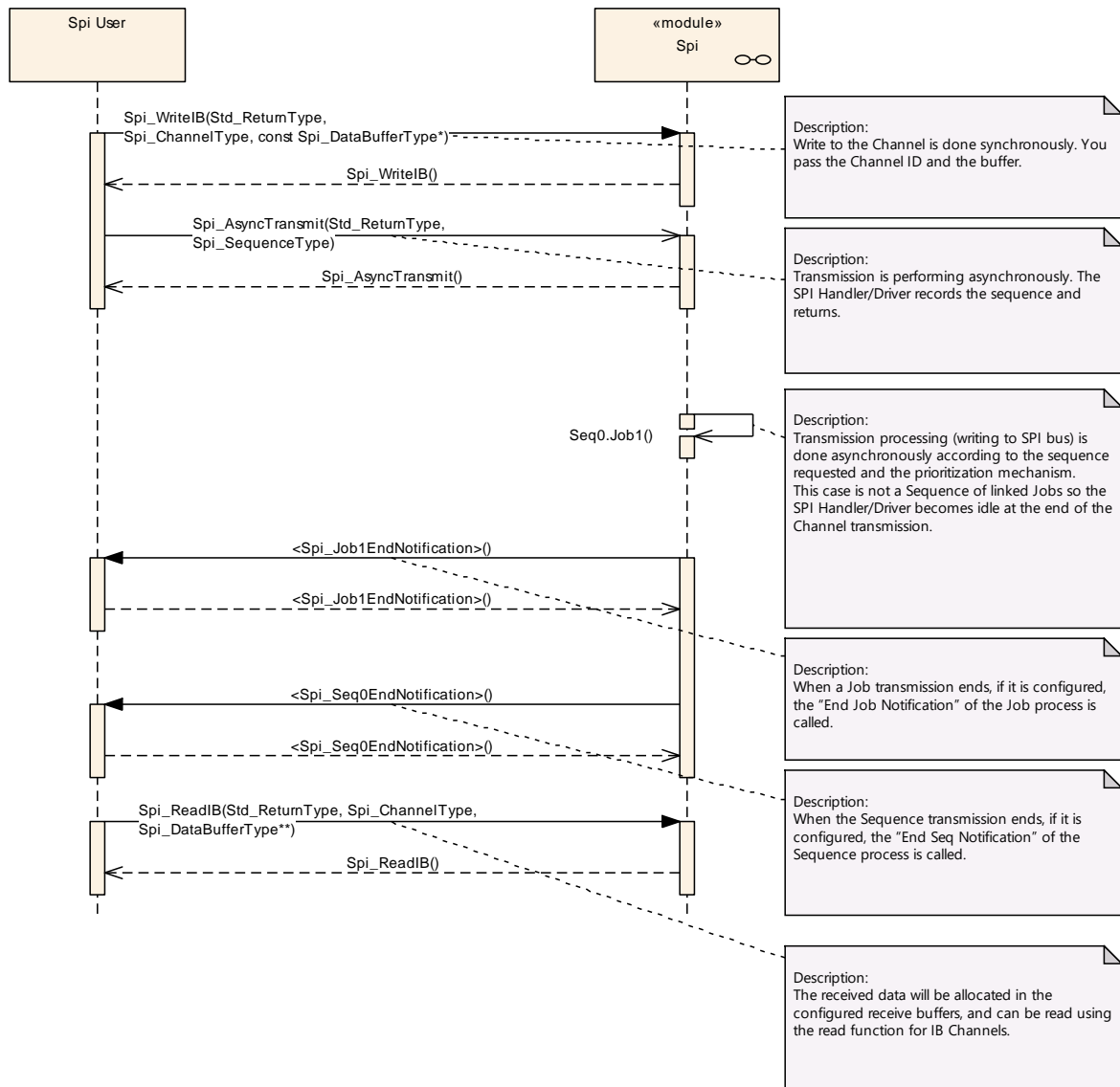
## 9.3 Write/AsyncTransmit/Read (IB)

### 9.3.1 One Channel, one Job then one Sequence

The following sequence diagram shows an example of Spi\_WriteIB / Spi\_AsyncTransmit / Spi\_ReadIB calls for a Sequence transmission with only one Job composed of only one Channel. Write or Read step could be skipped when Job is just reading or writing respectively.

Example: Channel ID 2 belongs to Job ID 1 which belongs to Sequence ID 0

Sequence	Job	Channel
ID0	ID1	ID2

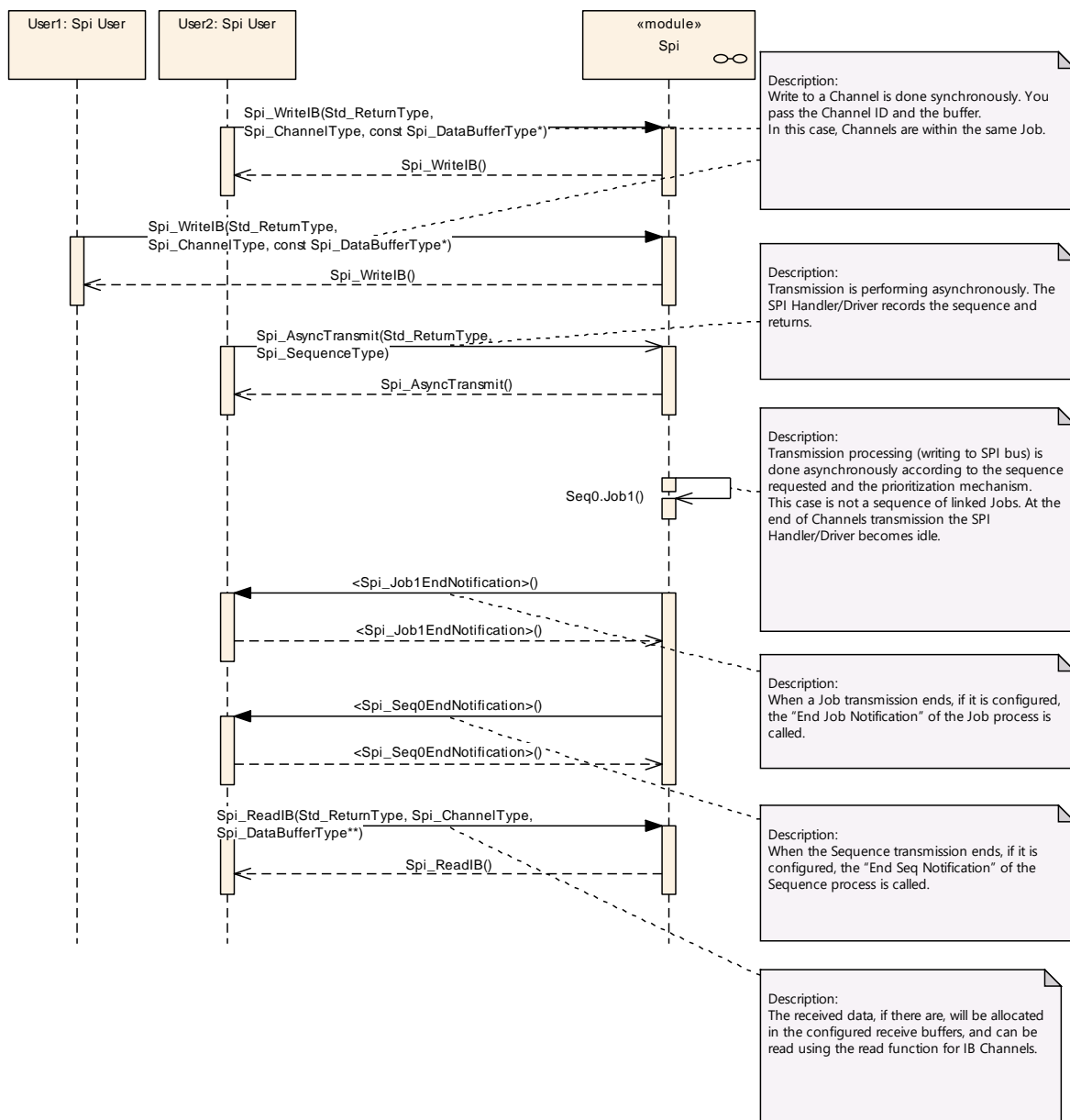


### 9.3.2 Many Channels, one Job then one Sequence

The following sequence diagram shows an example of Spi\_WriteIB / Spi\_AsyncTransmit / Spi\_ReadIB calls for a Sequence transmission with only one Job composed of many Channels. Write or Read steps could be skipped when Job is just reading or writing respectively.

Example: Channels ID 2 & 3 belong to Job ID 1 which belongs to Sequence ID 0

Sequence	Job	Channel
ID0	ID1	ID2
		ID3

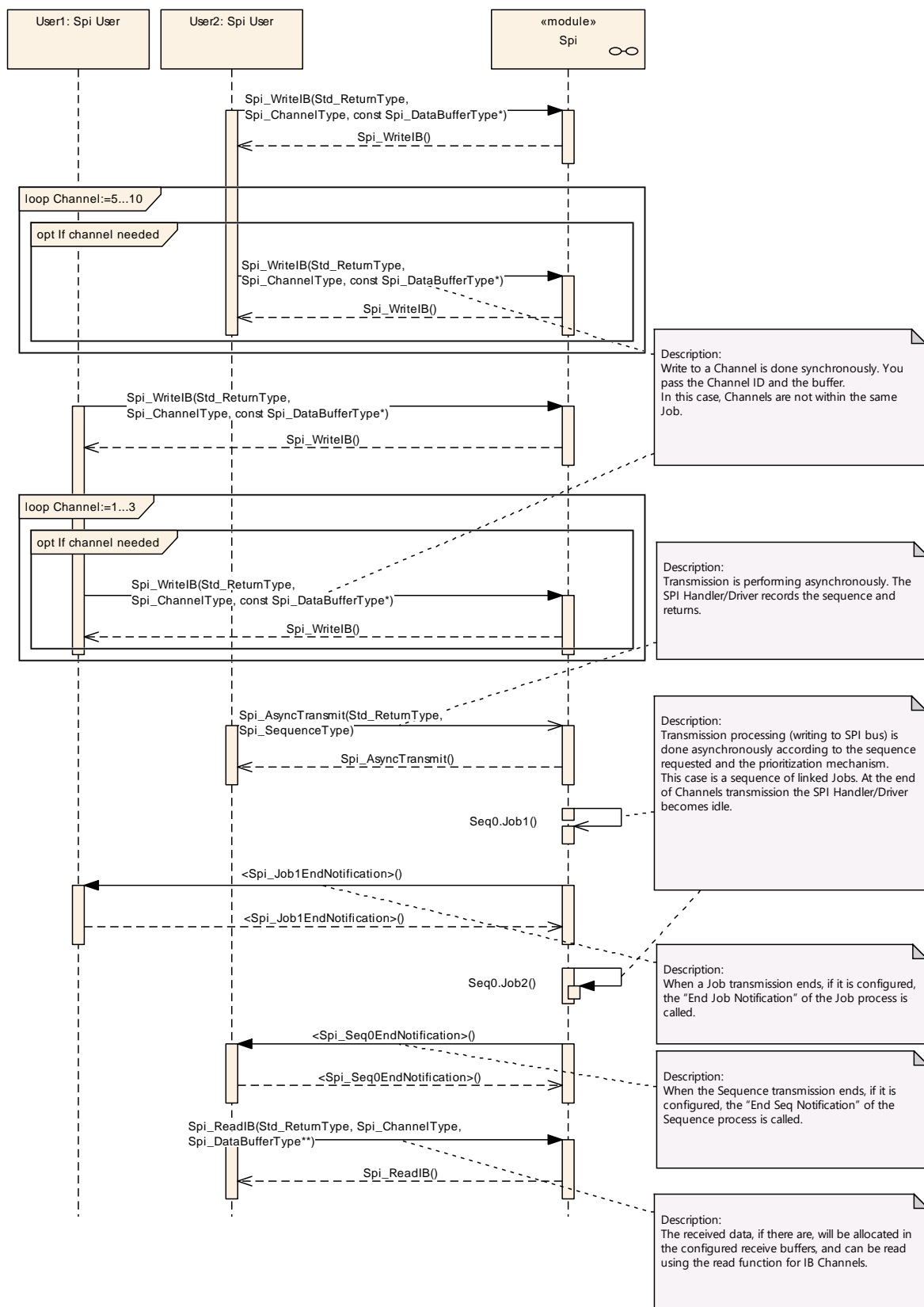


### 9.3.3 Many Channels, many Jobs and one Sequence

The following sequence diagram shows an example of Spi\_WriteIB / Spi\_AsyncTransmit / Spi\_ReadIB calls for a Sequence transmission of linked Jobs. Write or Read steps could be skipped when Jobs are just reading or writing respectively.

Example: Channels ID 0 to 3 belong to Job ID 1 (higher priority), Channels ID 4 to 10 belong to Job ID 2 (Lower priority) which has not an end notification function. These Jobs belong to the same Sequence ID 0

<b>Sequence</b>	<b>Job</b>		<b>Channel</b>
	<b>Name</b>	<b>Priority</b>	
ID0	ID1	High	ID0...ID3
	ID2	Low	ID4...ID10

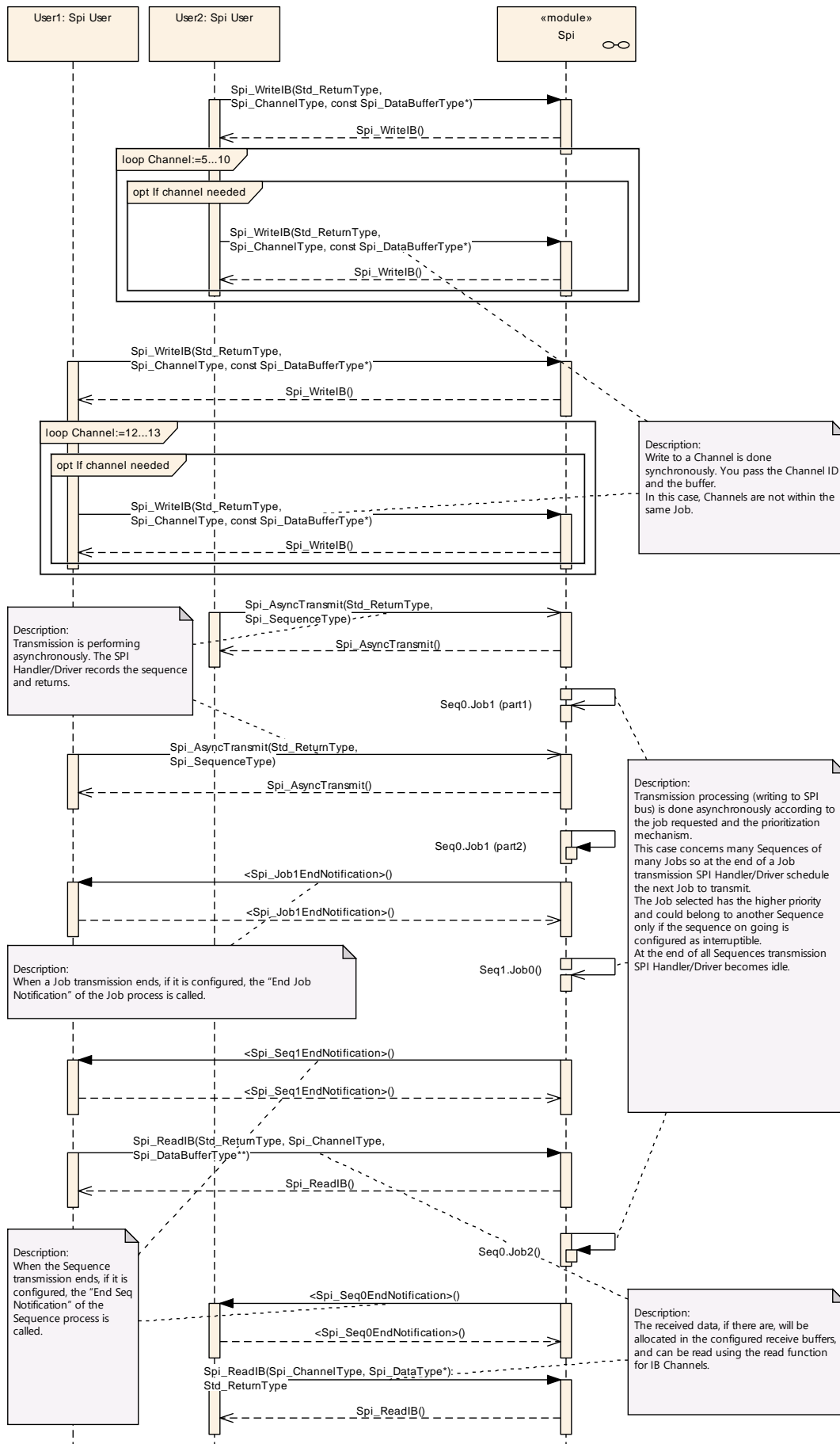


### 9.3.4 Many Channels, many Jobs and many Sequences

The following sequence diagram shows an example of Spi\_WriteIB / Spi\_AsyncTransmit / Spi\_ReadIB calls for Sequences transmission. Write or Read steps could be skipped when Jobs are just reading or writing respectively.

Example: Channels ID 0 to 3 belong to Job ID 1 (high priority 2), Channels ID 4 to 10 belong to Job ID 2 (Low priority 1) which has not an end notification function. These Jobs belong to the same Sequence ID 0 which is configured as interruptible. Channels ID 11 to 13 belong to Job ID 0 (higher priority 3) which belongs to Sequence ID 1 which is configured as not interruptible.

<b>Sequence</b>		<b>Job</b>		<b>Channel</b>
<b>Name</b>	<b>Interruptible</b>	<b>Name</b>	<b>Priority</b>	
ID0	Yes	ID1	2	ID0...ID3
		ID2	1	ID4...ID10
ID1	No	ID0	3	ID11...ID13



## 9.4 Setup/AsyncTransmit (EB)

### 9.4.1 Variable Number of Data / Constant Number of Data

**[SWS\_Spi\_00077]** 「To transmit a variable number of data, it is mandatory to call the `Spi_SetupEB` function to store new parameters within SPI Handler/Driver before each `Spi_AsyncTransmit` function call.」(SRS\_Spi\_12198, SRS\_Spi\_12200, SRS\_Spi\_12201)

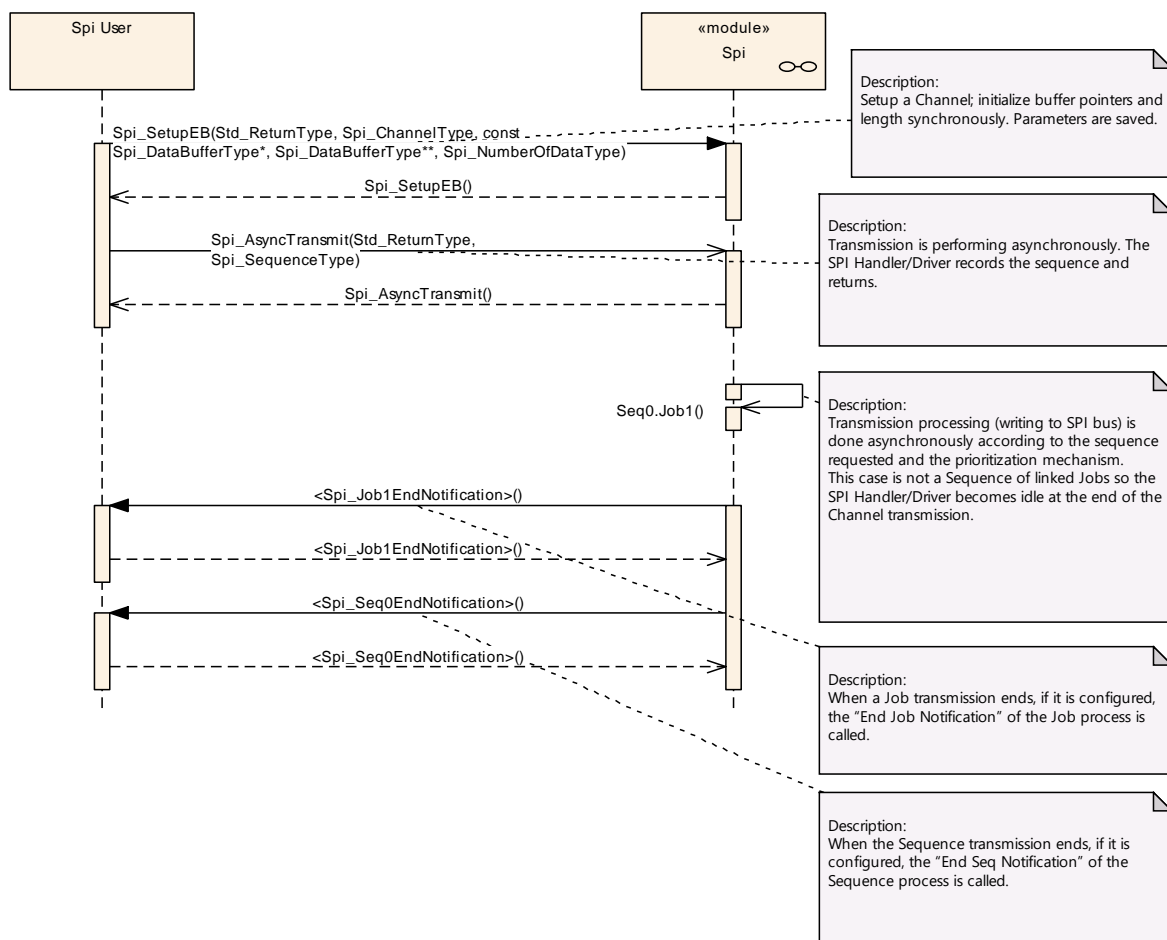
**[SWS\_Spi\_00078]** 「To transmit a constant number of data, it is only mandatory to call the `Spi_SetupEB` function to store parameters within SPI Handler/Driver before the first `Spi_AsyncTransmit` function call.」(SRS\_Spi\_12253, SRS\_Spi\_12262, SRS\_Spi\_12202)

### 9.4.2 One Channel, one Job then one Sequence

The following sequence diagram shows an example of `Spi_SetupEB` / `Spi_AsyncTransmit` calls for a Sequence transmission with only one Job composed of only one Channel. Write or Read accesses are “User Dependant” and could be skipped when Job is just reading or writing respectively.

Example: Channel ID 2 belongs to Job ID 1 which belongs to Sequence ID 0

<i><b>Sequence</b></i>	<i><b>Job</b></i>	<i><b>Channel</b></i>
ID0	ID1	ID2

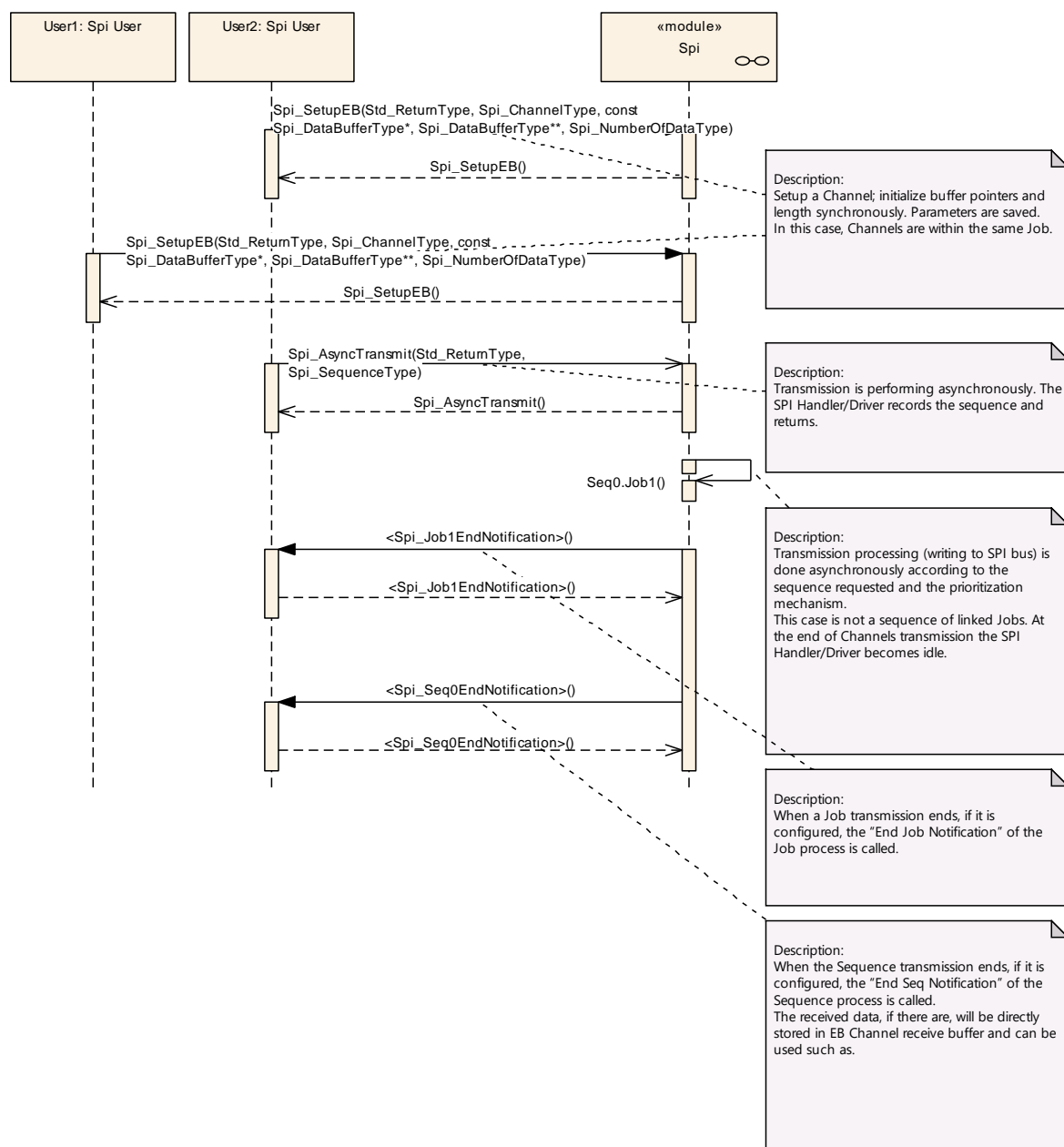


### 9.4.3 Many Channels, one Job then one Sequence

The following sequence diagram shows an example of `Spi_SetupEB` / `Spi_AsyncTransmit` calls for a Sequence transmission with only one Job composed of many Channels. Write or Read accesses are “User Dependant” and could be skipped when Job is just reading or writing respectively.

Example: Channels ID 2 & 3 belong to Job ID 1 which belongs to Sequence ID 0

Sequence	Job	Channel
ID0	ID1	ID2
		ID3

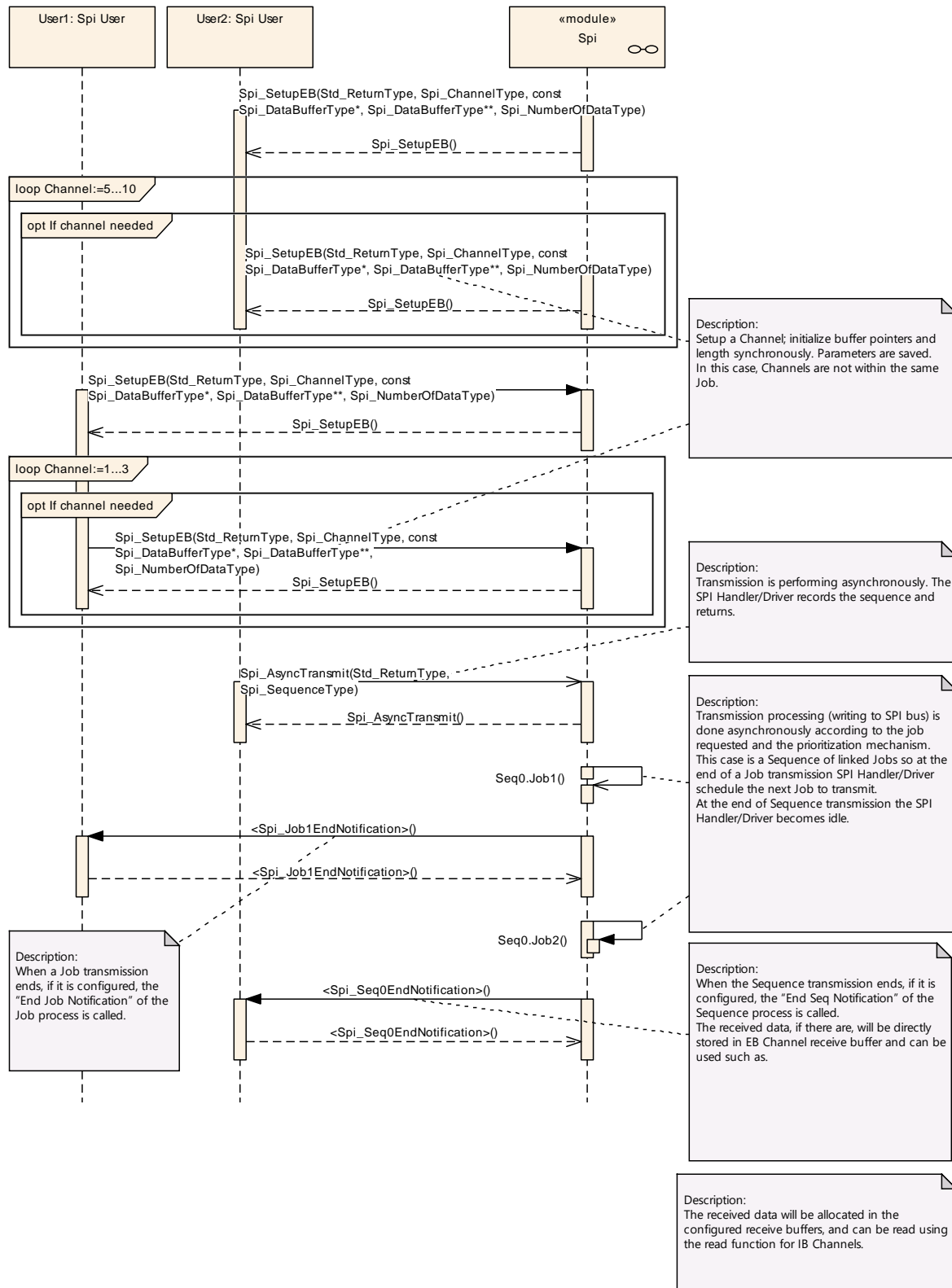


#### 9.4.4 Many Channels, many Jobs and one Sequence

The following sequence diagram shows an example of `Spi_SetupEB` / `Spi_AsyncTransmit` calls for a Sequence transmission of linked Jobs. Write or Read accesses are “User Dependant” and could be skipped when Job is just reading or writing respectively.

Example: Channels ID 0 to 3 belong to Job ID 1 (higher priority), Channels ID 4 to 10 belong to Job ID 2 (Lower priority) which has not an end notification function. These Jobs belong to the same Sequence ID 0

<b>Sequence</b>	<b>Job</b>	<b>Channel</b>
ID0	ID1	ID0...ID3
	ID2	ID4...ID10



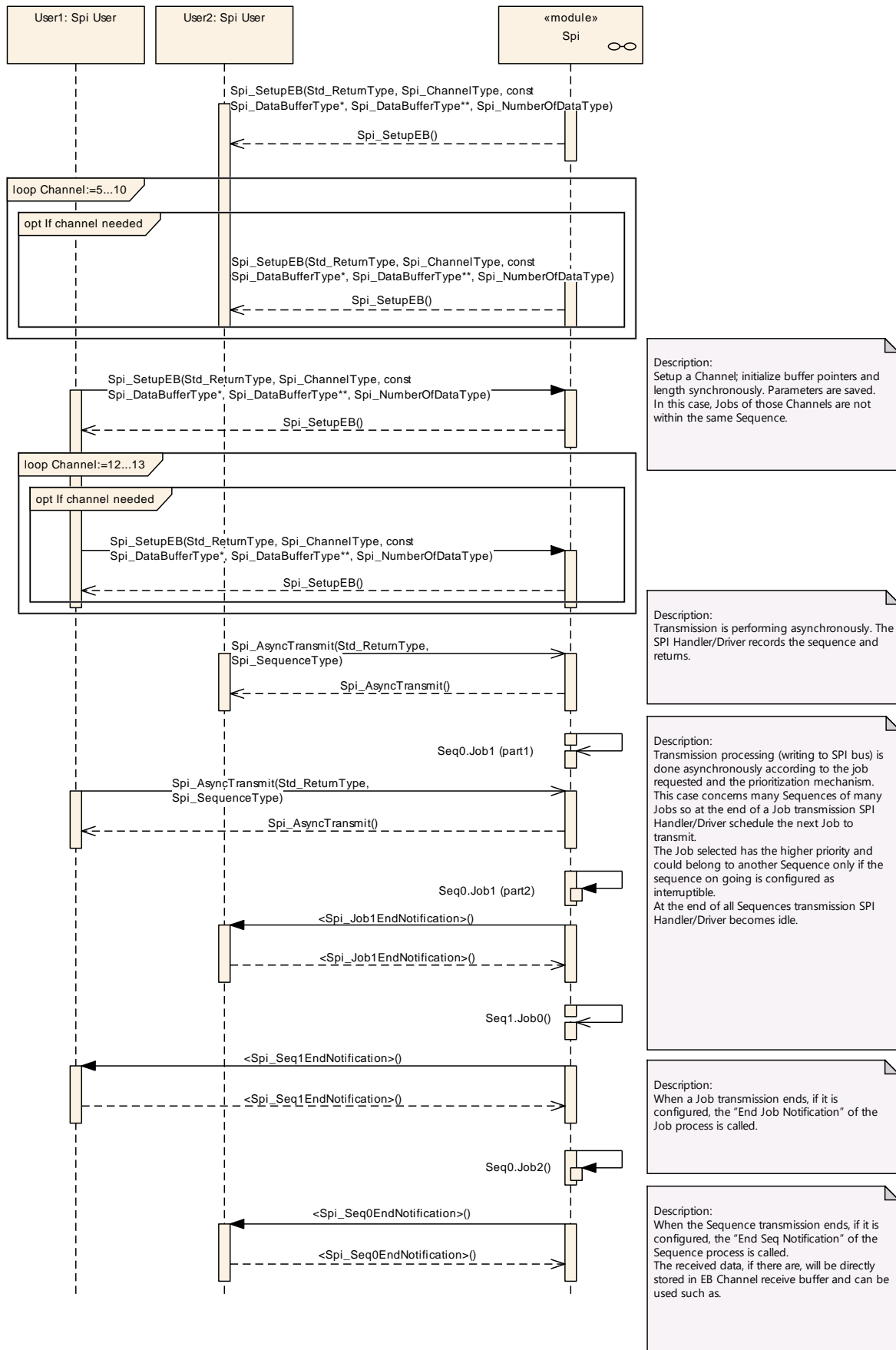
### 9.4.5 Many Channels, many Jobs and many Sequences

The following sequence diagram shows an example of `Spi_SetupEB` / `Spi_AsyncTransmit` calls for Sequences transmission. Write or Read accesses are “User Dependant” and could be skipped when Job is just reading or writing respectively.

Example: Channels ID 0 to 3 belong to Job ID 1 (high priority 2), Channels ID 4 to 10 belong to Job ID 2 (Low priority 1) which has not an end notification function. These Jobs belong to the same Sequence ID 0 which is configured as interruptible.

Channels ID 11 to 13 belong to Job ID 0 (higher priority 3) which belongs to Sequence ID 1 which is configured as not interruptible.

<b>Sequence</b>		<b>Job</b>		<b>Channel</b>
<b>Name</b>	<b>Interruptible</b>	<b>Name</b>	<b>Priority</b>	
ID0	Yes	ID1	2	ID0...ID3
		ID2	1	ID4...ID10
ID1	No	ID0	3	ID11...ID13



## 9.5 Mixed Jobs Transmission

All kind of mixed Jobs transmission is possible according to the Channels configuration and the priority requirement inside Sequences.

The user knows which Channels are in use. Then, according to the types of these Channels, the appropriate methods shall be called.

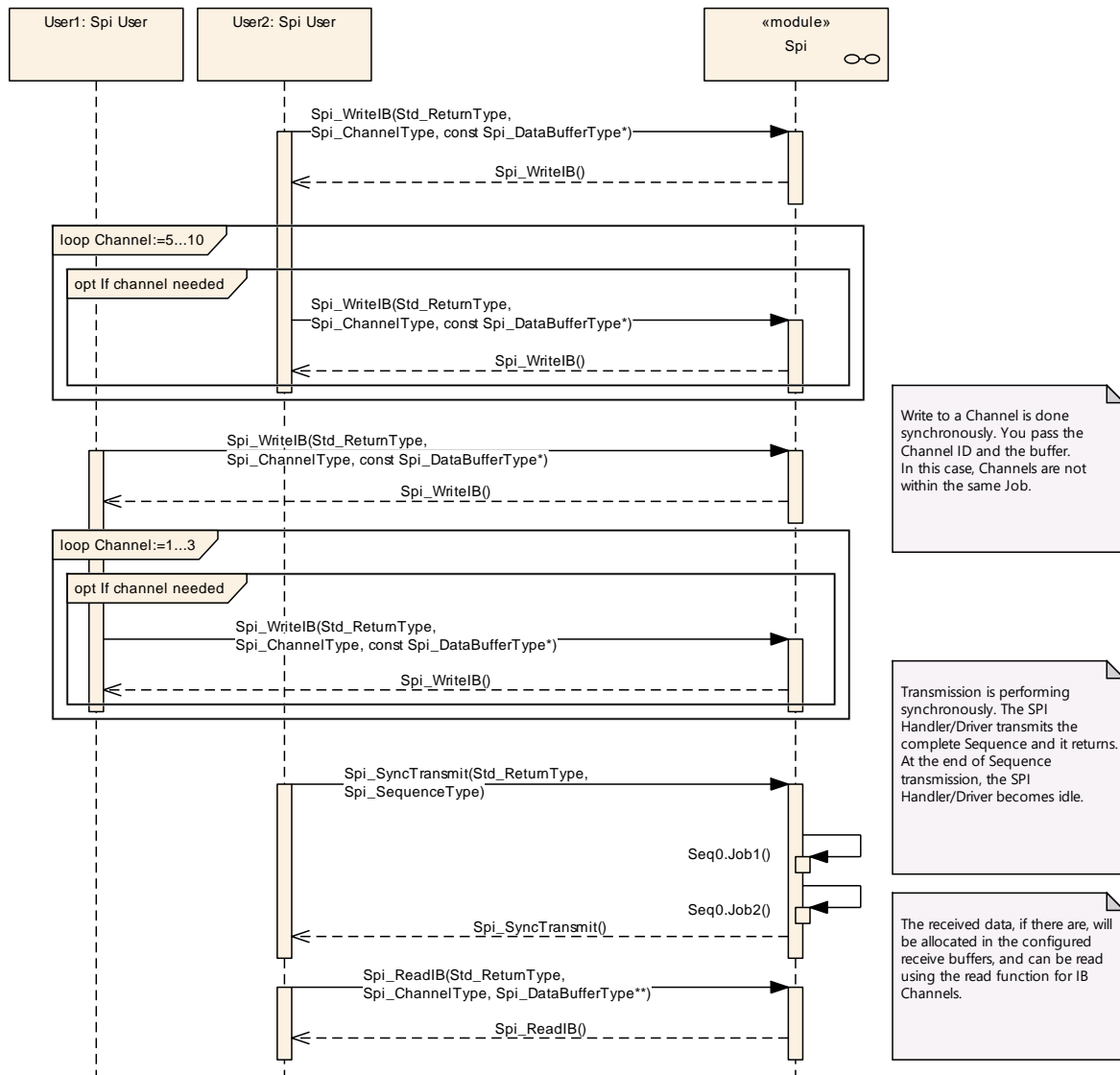
## 9.6 LEVEL 0 SyncTransmit diagrams

### 9.6.1 Write/SyncTransmit/Read (IB): Many Channels, many Jobs and one Sequence

The following sequence diagram shows an example of Spi\_WriteIB / Spi\_SyncTransmit / Spi\_ReadIB calls for a Sequence transmission of linked Jobs. Write or Read steps could be skipped when Jobs are just reading or writing respectively.

Example: Channels ID 0 to 3 belong to Job ID 1 (higher priority), Channels ID 4 to 10 belong to Job ID 2 (Lower priority). These Jobs belong to the same Sequence ID 0

<b>Sequence</b>	<b>Job</b>		<b>Channel</b>
	<b>Name</b>	<b>Priority</b>	
ID0	ID1	High	ID0...ID3
	ID2	Low	ID4...ID10

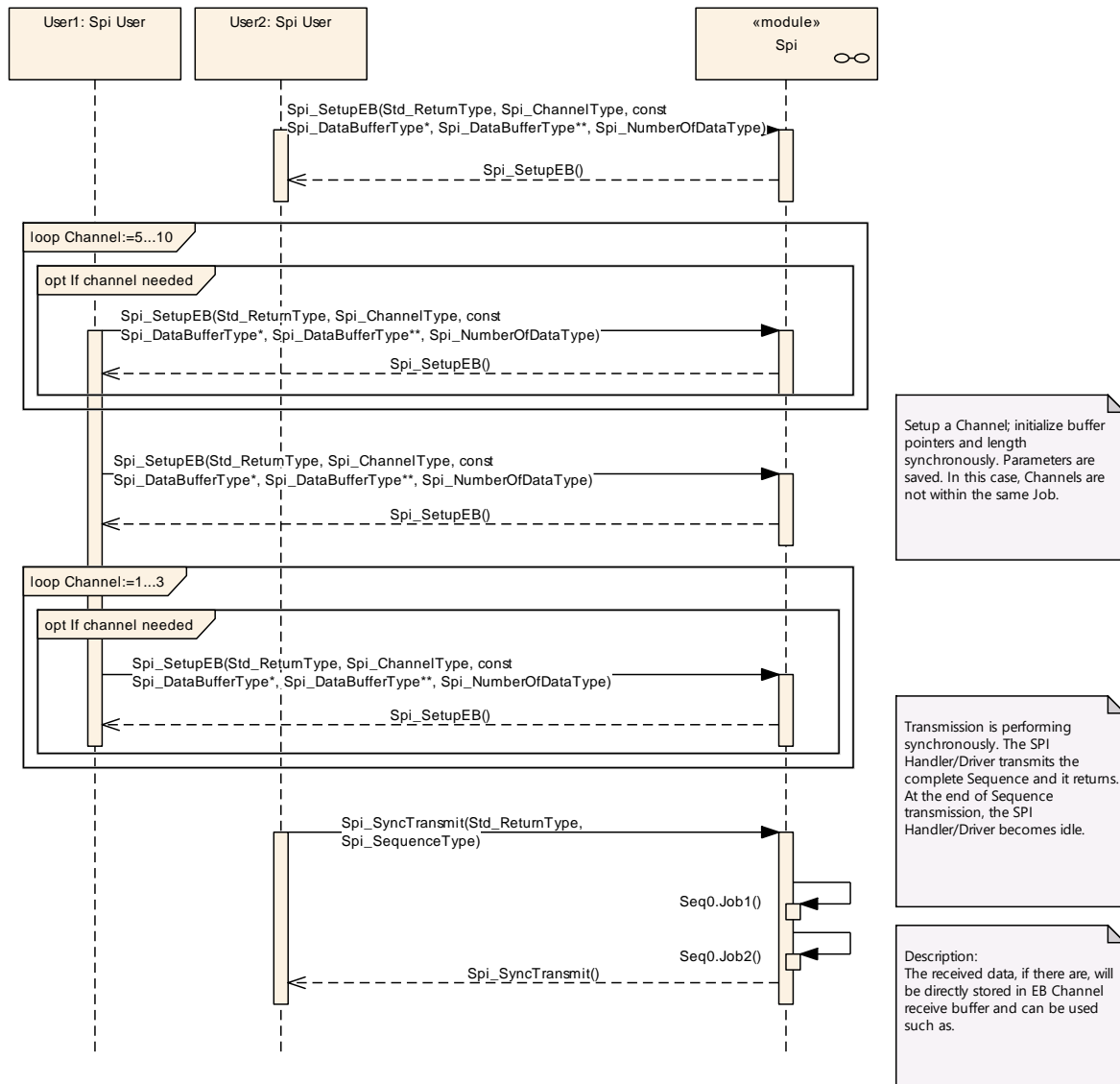


### 9.6.2 Setup/SyncTransmit (EB): Many Channels, many Jobs and one Sequence

The following sequence diagram shows an example of `Spi_SetupEB` / `Spi_SyncTransmit` calls for a Sequence transmission of linked Jobs. Write or Read accesses are “User Dependant” and could be skipped when Job is just reading or writing respectively.

Example: Channels ID 0 to 3 belong to Job ID 1 (higher priority), Channels ID 4 to 10 belong to Job ID 2 (Lower priority). These Jobs belong to the same Sequence ID 0

Sequence	Job	Channel
ID0	ID1	ID0...ID3
	ID2	ID4...ID10



## 10 Configuration specification

### 10.1 How to read this chapter

For details refer to the chapter 10.1 “Introduction to configuration specification” in *SWS\_BSWGeneral*.

### 10.2 Containers and configuration parameters

The following chapters summarize all configuration parameters. The detailed meanings of the parameters are described in Chapter 7 and Chapter 8. Further hardware / implementation specific parameters can be added if necessary.

**[SWS\_Spi\_00390]** 「The SPI module shall reject configurations with partition mappings which are not supported by the implementation. 」()

#### 10.2.1 Spi

<b>SWS Item</b>	<b>ECUC_Spi_00103 :</b>
<b>Module Name</b>	<i>Spi</i>
<b>Module Description</b>	Configuration of the Spi (Serial Peripheral Interface) module.
<b>Post-Build Variant Support</b>	true
<b>Supported Config Variants</b>	VARIANT-LINK-TIME, VARIANT-POST-BUILD, VARIANT-PRE-COMPILE

<b>Included Containers</b>		
<b>Container Name</b>	<b>Multiplicity</b>	<b>Scope / Dependency</b>
SpiDemEventParameterRefs	0..1	Container for the references to DemEventParameter elements which shall be invoked using the API Dem_SetEventStatus in case the corresponding error occurs. The EventId is taken from the referenced DemEventParameter's DemEventId symbolic value. The standardized errors are provided in this container and can be extended by vendor-specific error references.
SpiDriver	1	This container contains the configuration parameters and sub containers of the AUTOSAR Spi module.
SpiGeneral	1	General configuration settings for SPI-Handler
SpiPublishedInformation	1	Container holding all SPI specific published information parameters

#### 10.2.2 SpiDemEventParameterRefs

<b>SWS Item</b>	<b>ECUC_Spi_00240 :</b>
<b>Container Name</b>	SpiDemEventParameterRefs
<b>Parent Container</b>	Spi
<b>Description</b>	Container for the references to DemEventParameter elements which shall be invoked using the API Dem_SetEventStatus in case the corresponding error occurs. The EventId is taken from the referenced DemEventParameter's DemEventId symbolic value. The standardized errors are provided in this container and can be extended by vendor-specific error references.
<b>Configuration Parameters</b>	

<b>SWS Item</b>	<b>ECUC_Spi_00241 :</b>		
<b>Name</b>	SPI_E_HARDWARE_ERROR		
<b>Parent Container</b>	SpiDemEventParameterRefs		
<b>Description</b>	Reference to configured DEM event to report "Hardware failure". If the reference is not configured the error shall not be reported.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	Symbolic name reference to [ DemEventParameter ]		
<b>Post-Build Variant Multiplicity</b>	false		
<b>Post-Build Variant Value</b>	false		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

**No Included Containers**

### 10.2.3 SpiGeneral

<b>SWS Item</b>	<b>ECUC_Spi_00225 :</b>
<b>Container Name</b>	SpiGeneral
<b>Parent Container</b>	Spi
<b>Description</b>	General configuration settings for SPI-Handler
<b>Configuration Parameters</b>	

<b>SWS Item</b>	<b>ECUC_Spi_00226 :</b>		
<b>Name</b>	SpiCancelApi		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Switches the Spi_Cancel function ON or OFF.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00227 :</b>		
<b>Name</b>	SpiChannelBuffersAllowed		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Selects the SPI Handler/Driver Channel Buffers usage allowed and delivered.  IB = 0; EB = 1; IB/EB = 2;		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 2		

<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00228 :</b>		
<b>Name</b>	SpiDevErrorDetect		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Switches the development error detection and notification on or off. <ul style="list-style-type: none"> <li>true: detection and notification is enabled.</li> <li>false: detection and notification is disabled.</li> </ul>		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	false		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00229 :</b>		
<b>Name</b>	SpiHwStatusApi		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Switches the Spi_GetHWUnitStatus function ON or OFF.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00230 :</b>		
<b>Name</b>	SpiInterruptibleSeqAllowed		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Switches the Interruptible Sequences handling functionality ON or OFF.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local dependency: This parameter depends on SPI_LEVEL_DELIVERED value. It is only used for SPI_LEVEL_DELIVERED configured to 1 or 2.		

<b>SWS Item</b>	<b>ECUC_Spi_00231 :</b>		
<b>Name</b>	SpiLevelDelivered		

<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Selects the SPI Handler/Driver level of scalable functionality that is available and delivered.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 2		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00242 :</b>		
<b>Name</b>	SpiMainFunctionPeriod		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	This parameter defines the cycle time of the function Spi_MainFunction_Handling in seconds. The parameter is not used by the driver it self, but it is used by upper layer.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucFloatParamDef		
<b>Range</b>	]0 .. INF[		
<b>Default value</b>	0.01		
<b>Post-Build Variant Multiplicity</b>	false		
<b>Post-Build Variant Value</b>	false		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00237 :</b>		
<b>Name</b>	SpiSupportConcurrentSyncTransmit		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Specifies whether concurrent Spi_SyncTransmit() calls for different sequences shall be configurable.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

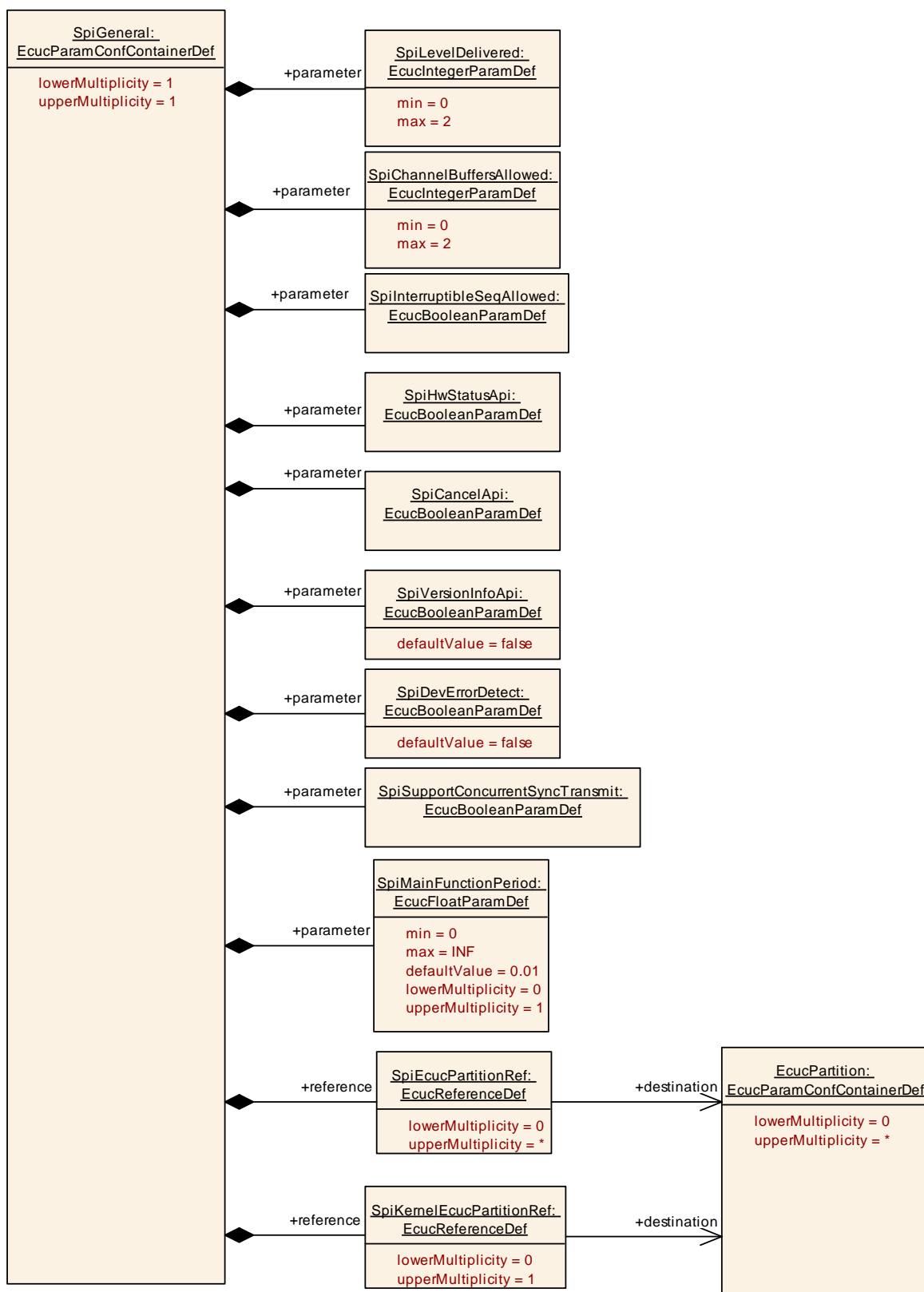
<b>SWS Item</b>	<b>ECUC_Spi_00232 :</b>		
<b>Name</b>	SpiVersionInfoApi		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Switches the Spi_GetVersionInfo function ON or OFF.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	false		

<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00244 :</b>		
<b>Name</b>	SpiEcucPartitionRef		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Maps the SPI driver to zero or multiple ECUC partitions to make the driver API available in the according partition.		
<b>Multiplicity</b>	0..*		
<b>Type</b>	Reference to [ EcucPartition ]		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: ECU		

<b>SWS Item</b>	<b>ECUC_Spi_00245 :</b>		
<b>Name</b>	SpiKernelEcucPartitionRef		
<b>Parent Container</b>	SpiGeneral		
<b>Description</b>	Maps the SPI kernel to zero or one ECUC partitions to assign the driver kernel to a certain core. The ECUC partition referenced is a subset of the ECUC partitions where the SPI driver is mapped to.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	Reference to [ EcucPartition ]		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: ECU		

<b>No Included Containers</b>
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[SWS\_Spi\_CONSTR\_00001] [The ECUC partitions referenced by SpiKernelEcucPartitionRef shall be a subset of the ECUC partitions referenced by SpiEcucPartitionRef.]()

[SWS\_Spi\_CONSTR\_00003] [If SpiEcucPartitionRef references one or more ECUC partitions, SpiKernelEcucPartitionRef shall have a multiplicity of one and reference one of these ECUC partitions as well.]( )

#### 10.2.4 SpiSequence

<b>SWS Item</b>	<b>ECUC_Spi_00106 :</b>
<b>Container Name</b>	SpiSequence
<b>Parent Container</b>	SpiDriver
<b>Description</b>	All data needed to configure one SPI-sequence
<b>Configuration Parameters</b>	

<b>SWS Item</b>	<b>ECUC_Spi_00222 :</b>		
<b>Name</b>	SpiInterruptibleSequence		
<b>Parent Container</b>	SpiSequence		
<b>Description</b>	This parameter allows or not this Sequence to be suspended by another one.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local dependency: This SPI_INTERRUPTIBLE_SEQ_ALLOWED parameter as to be configured as ON.		

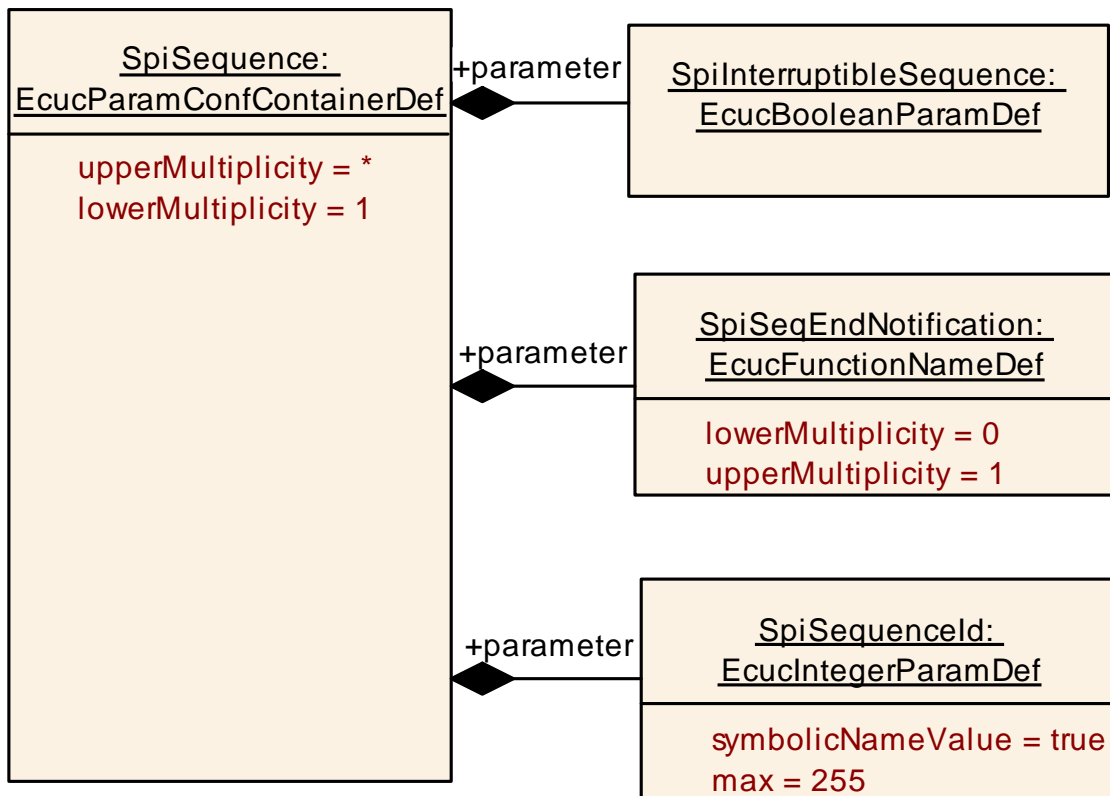
<b>SWS Item</b>	<b>ECUC_Spi_00223 :</b>		
<b>Name</b>	SpiSeqEndNotification		
<b>Parent Container</b>	SpiSequence		
<b>Description</b>	This parameter is a reference to a notification function.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucFunctionNameDef		
<b>Default value</b>	--		
<b>maxLength</b>	--		
<b>minLength</b>	--		
<b>regularExpression</b>	--		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00224 :</b>		
<b>Name</b>	SpiSequenceId		
<b>Parent Container</b>	SpiSequence		

<b>Description</b>	SPI Sequence ID, used as parameter in SPI API functions.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef (Symbolic Name generated for this parameter)		
<b>Range</b>	0 .. 255		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: ECU		

<b>SWS Item</b>	<b>ECUC_Spi_00221 :</b>		
<b>Name</b>	SpiJobAssignment		
<b>Parent Container</b>	SpiSequence		
<b>Description</b>	A sequence references several jobs, which are executed during a communication sequence		
<b>Multiplicity</b>	1..*		
<b>Type</b>	Reference to [ SpiJob ]		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

**No Included Containers**



### 10.2.5 SpiChannel

<b>SWS Item</b>	<b>ECUC_Spi_00104 :</b>		
<b>Container Name</b>	SpiChannel		
<b>Parent Container</b>	SpiDriver		
<b>Description</b>	All data needed to configure one SPI-channel		
<b>Configuration Parameters</b>			

<b>SWS Item</b>	<b>ECUC_Spi_00200 :</b>		
<b>Name</b>	SpiChannelId		
<b>Parent Container</b>	SpiChannel		
<b>Description</b>	SPI Channel ID, used as parameter in SPI API functions.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef (Symbolic Name generated for this parameter)		
<b>Range</b>	0 .. 255		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00201 :</b>		
<b>Name</b>	SpiChannelType		
<b>Parent Container</b>	SpiChannel		
<b>Description</b>	Buffer usage with EB/IB channel.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucEnumerationParamDef		

Range	EB	External Buffer	
	IB	Internal Buffer	
Post-Build Variant Value	true		
Value Configuration Class	Pre-compile time	X	VARIANT-PRE-COMPILE
	Link time	X	VARIANT-LINK-TIME
	Post-build time	X	VARIANT-POST-BUILD
Scope / Dependency	scope: local dependency: SPI_CHANNEL_BUFFERS_ALLOWED		

<b>SWS Item</b>	<b>ECUC_Spi_00202 :</b>		
<b>Name</b>	SpiDataWidth		
<b>Parent Container</b>	SpiChannel		
<b>Description</b>	This parameter is the width of a transmitted data unit.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	1 .. 32		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00203 :</b>		
<b>Name</b>	SpiDefaultData		
<b>Parent Container</b>	SpiChannel		
<b>Description</b>	The default data to be transmitted when (for internal buffer or external buffer) the pointer passed to Spi_WriteIB (for internal buffer) or to Spi_SetupEB (for external buffer) is NULL.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 4294967295		
<b>Default value</b>	--		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

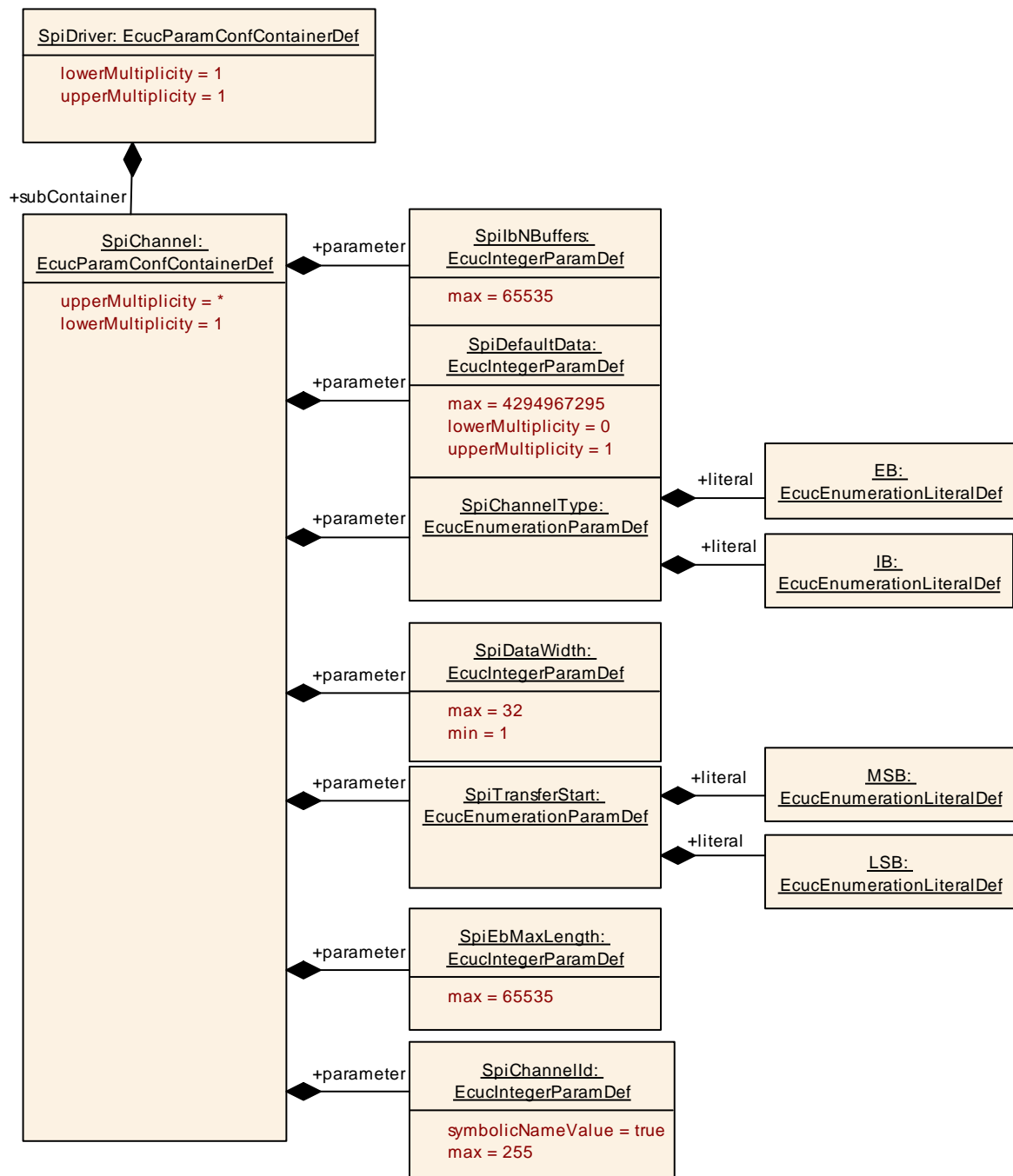
<b>SWS Item</b>	<b>ECUC_Spi_00204 :</b>		
<b>Name</b>	SpiEbMaxLength		
<b>Parent Container</b>	SpiChannel		
<b>Description</b>	This parameter contains the maximum size (number of data elements) of data buffers in case of EB Channels and only.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 65535		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		

<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local dependency: The SPI_CHANNEL_TYPE parameter has to be configured as EB for this Channel. The SPI_CHANNEL_BUFFERS_ALLOWED parameter has to be configured as 1 or 2.		

<b>SWS Item</b>	<b>ECUC_Spi_00205 :</b>		
<b>Name</b>	SpiIbNBuffers		
<b>Parent Container</b>	SpiChannel		
<b>Description</b>	This parameter contains the maximum number of data buffers in case of IB Channels and only.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 65535		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local dependency: The SPI_CHANNEL_TYPE parameter has to be configured as IB for this Channel. The SPI_CHANNEL_BUFFERS_ALLOWED parameter has to be configured as 0 or 2.		

SWS Item	ECUC_Spi_00206 :		
Name	SpiTransferStart		
Parent Container	SpiChannel		
Description	This parameter defines the first starting bit for transmission.		
Multiplicity	1		
Type	EcucEnumerationParamDef		
Range	LSB	Transmission starts with the Least Significant Bit first	
	MSB	Transmission starts with the Most Significant Bit first	
Post-Build Variant Value	true		
Value Configuration Class	Pre-compile time	X	VARIANT-PRE-COMPILE
	Link time	X	VARIANT-LINK-TIME
	Post-build time	X	VARIANT-POST-BUILD
Scope / Dependency	scope: local		

**No Included Containers**



## 10.2.6 SpiChannelList

<b>SWS Item</b>	<b>ECUC_Spi_00233 :</b>
<b>Container Name</b>	SpiChannelList
<b>Parent Container</b>	SpiJob
<b>Description</b>	References to SPI channels and their order within the Job.
<b>Configuration Parameters</b>	

<b>SWS Item</b>	<b>ECUC_Spi_00234 :</b>
<b>Name</b>	SpiChannelIndex
<b>Parent Container</b>	SpiChannelList
<b>Description</b>	This parameter specifies the order of Channels within the Job.

<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 255		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00215 :</b>		
<b>Name</b>	SpiChannelAssignment		
<b>Parent Container</b>	SpiChannelList		
<b>Description</b>	A job reference to a SPI channel.		
<b>Multiplicity</b>	1		
<b>Type</b>	Reference to [ SpiChannel ]		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

**No Included Containers**

## 10.2.7 SpiJob

<b>SWS Item</b>	<b>ECUC_Spi_00105 :</b>
<b>Container Name</b>	SpiJob
<b>Parent Container</b>	SpiDriver
<b>Description</b>	All data needed to configure one SPI-Job, amongst others the connection between the internal SPI unit and the special settings for an external device is done.
<b>Configuration Parameters</b>	

<b>SWS Item</b>	<b>ECUC_Spi_00218 :</b>		
<b>Name</b>	SpiJobEndNotification		
<b>Parent Container</b>	SpiJob		
<b>Description</b>	This parameter is a reference to a notification function.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucFunctionNameDef		
<b>Default value</b>	--		
<b>maxLength</b>	--		
<b>minLength</b>	--		
<b>regularExpression</b>	--		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME

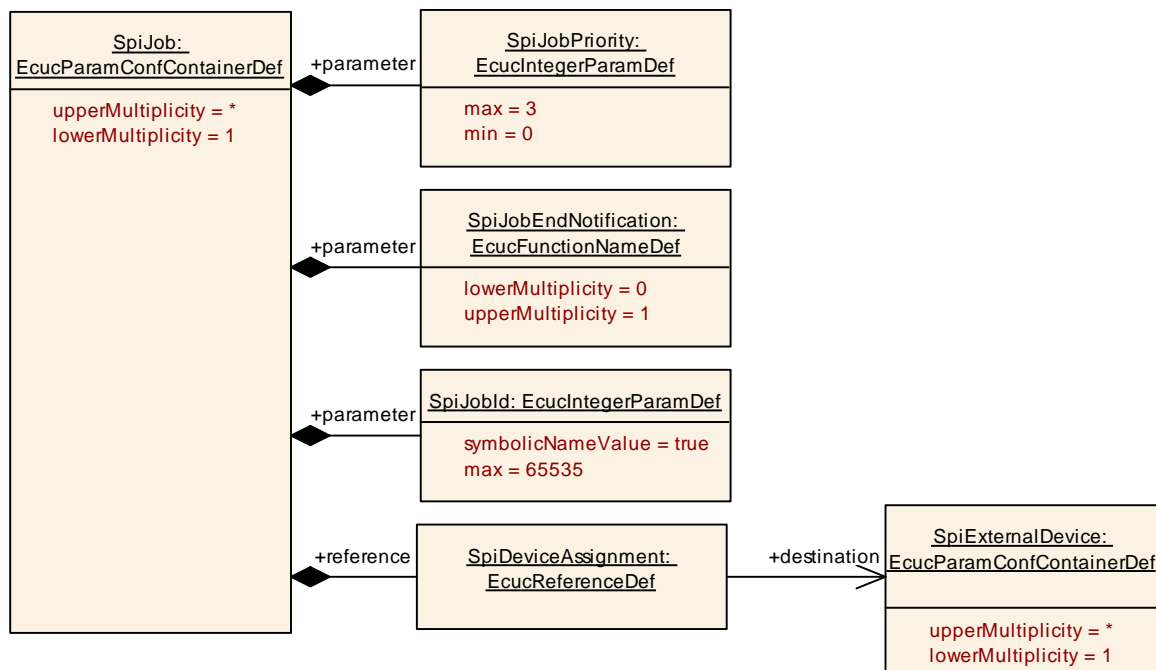
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00219 :</b>		
<b>Name</b>	SpiJobId		
<b>Parent Container</b>	SpiJob		
<b>Description</b>	SPI Job ID, used as parameter in SPI API functions.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef (Symbolic Name generated for this parameter)		
<b>Range</b>	0 .. 65535		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00220 :</b>		
<b>Name</b>	SpiJobPriority		
<b>Parent Container</b>	SpiJob		
<b>Description</b>	Priority set accordingly to SPI093: 0, lowest, 3, highest priority		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 3		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00216 :</b>		
<b>Name</b>	SpiDeviceAssignment		
<b>Parent Container</b>	SpiJob		
<b>Description</b>	Reference to the external device used by this job		
<b>Multiplicity</b>	1		
<b>Type</b>	Reference to [ SpiExternalDevice ]		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>Included Containers</b>		
<b>Container Name</b>	<b>Multiplicity</b>	<b>Scope / Dependency</b>
SpiChannellist	1..*	References to SPI channels and their order within the Job.



### 10.2.8 SpiExternalDevice

<b>SWS Item</b>	<b>ECUC_Spi_00207 :</b>		
<b>Container Name</b>	SpiExternalDevice		
<b>Parent Container</b>	SpiDriver		
<b>Description</b>	The communication settings of an external device. Closely linked to Spi-Job.		
<b>Configuration Parameters</b>			

<b>SWS Item</b>	<b>ECUC_Spi_00208 :</b>		
<b>Name</b>	SpiBaudrate		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	This parameter is the communication baudrate - This parameter allows using a range of values, from the point of view of configuration tools, from Hz up to MHz.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucFloatParamDef		
<b>Range</b>	[0 .. INF]		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00209 :</b>		
<b>Name</b>	SpiCsIdentifier		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	This parameter is the symbolic name to identify the Chip Select (CS) allocated to this Job.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucStringParamDef (Symbolic Name generated for this parameter)		
<b>Default value</b>	--		

<b>maxLength</b>	--		
<b>minLength</b>	--		
<b>regularExpression</b>	--		
<b>Post-Build Variant Value</b>	false		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00210 :</b>		
<b>Name</b>	SpiCsPolarity		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	This parameter defines the active polarity of Chip Select.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucEnumerationParamDef		
<b>Range</b>	HIGH	--	
	LOW	--	
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00239 :</b>		
<b>Name</b>	SpiCsSelection		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	When the Chip select handling is enabled (see SpiEnableCs), then this parameter specifies if the chip select is handled automatically by Peripheral HW engine or via general purpose IO by Spi driver.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucEnumerationParamDef		
<b>Range</b>	CS_VIA_GPIO	chip select handled via gpio by Spi driver.	
	CS_VIA_PERIPHERAL_ENGINE	chip select is handled automatically by Peripheral HW engine.	
<b>Default value</b>	CS_VIA_PERIPHERAL_ENGINE		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local dependency: SpiEnableCs		

<b>SWS Item</b>	<b>ECUC_Spi_00211 :</b>		
<b>Name</b>	SpiDataShiftEdge		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	This parameter defines the SPI data shift edge.		

Multiplicity	1		
Type	EcucEnumerationParamDef		
Range	LEADING	--	
	TRAILING	--	
Post-Build Variant Value	true		
Value Configuration Class	Pre-compile time	X	VARIANT-PRE-COMPILE
	Link time	X	VARIANT-LINK-TIME
	Post-build time	X	VARIANT-POST-BUILD
Scope / Dependency	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00212 :</b>		
<b>Name</b>	SpiEnableCs		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	This parameter enables or not the Chip Select handling functions. If this parameter is enabled then parameter SpiCsSelection further details the type of chip selection.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucBooleanParamDef		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00217 :</b>		
<b>Name</b>	SpiHwUnit		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	This parameter is the symbolic name to identify the HW SPI Hardware microcontroller peripheral allocated to this Job.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucEnumerationParamDef		
<b>Range</b>	CSIB0	--	
	CSIB1	--	
	CSIB2	--	
	CSIB3	--	
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00213 :</b>		
<b>Name</b>	SpiShiftClockIdleLevel		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	This parameter defines the SPI shift clock idle level.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucEnumerationParamDef		
<b>Range</b>	HIGH	--	
	LOW	--	
<b>Post-Build Variant</b>	true		

<b>Value</b>			
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00214 :</b>		
<b>Name</b>	SpiTimeClk2Cs		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	Timing between clock and chip select (in seconds) - This parameter allows to use a range of values from 0 up to 0.0001 seconds. The real configuration-value used in software BSW-SPI is calculated out of this by the generator-tools		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucFloatParamDef		
<b>Range</b>	[0 .. 1E-4]		
<b>Default value</b>	--		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

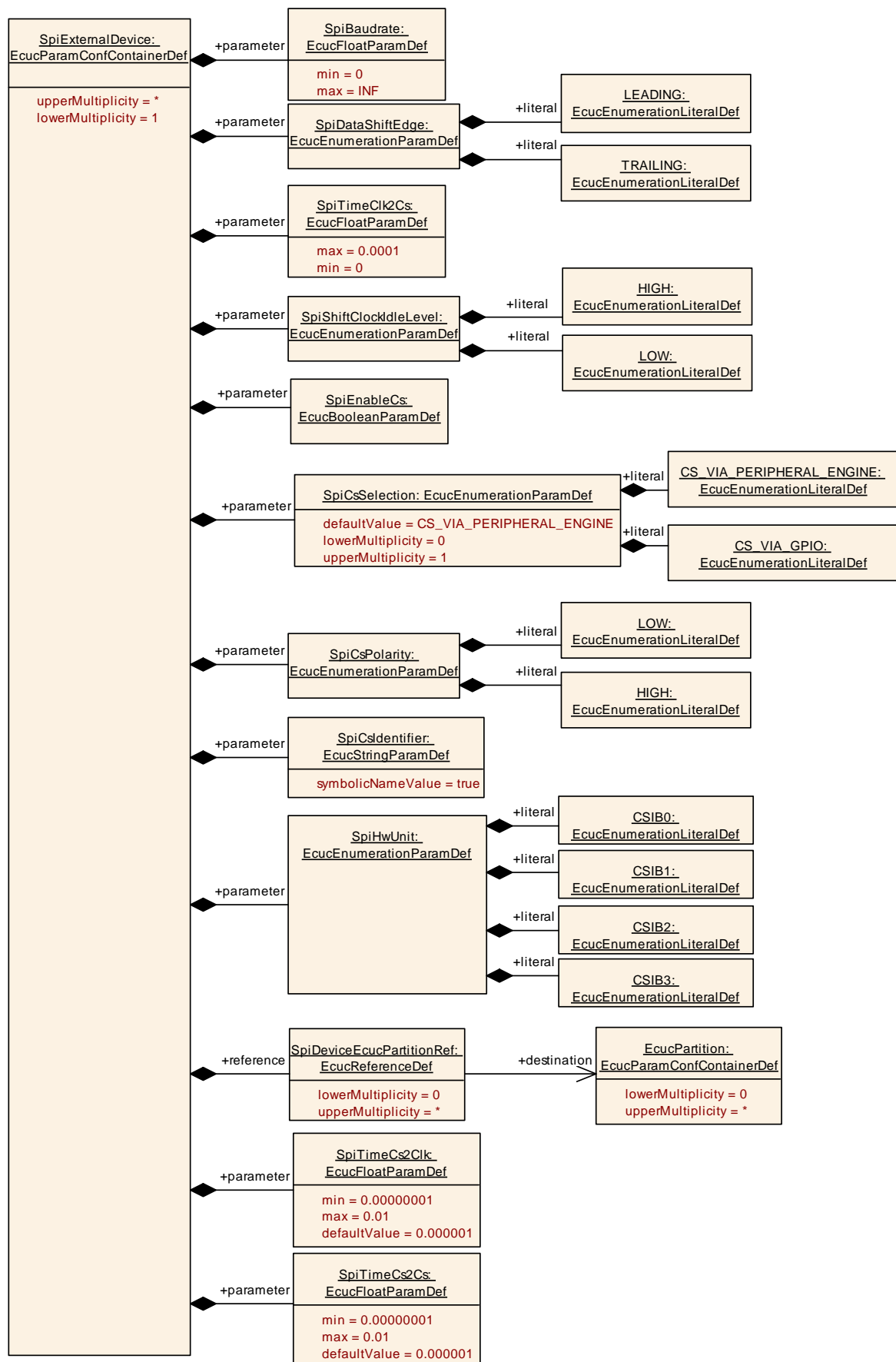
<b>SWS Item</b>	<b>ECUC_Spi_00247 :</b>		
<b>Name</b>	SpiTimeCs2Clk		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	Timing between chip select assertion and clock (in seconds) - This parameter allows to use a range of values from 10ns up to 0.01 seconds. The real configuration-value used in software BSW-SPI is calculated out of this by the generator-tools.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucFloatParamDef		
<b>Range</b>	[1E-8 .. 0.01]		
<b>Default value</b>	1E-6		
<b>Post-Build Variant Value</b>	true		
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00248 :</b>		
<b>Name</b>	SpiTimeCs2Cs		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	Timing between the negation of the chip select at the end of frame and the assertion of the chip select at the beginning of the next frame (in seconds) - This parameter allows to use a range of values from 10ns up to 0.01 seconds. The real configuration-value used in software BSW-SPI is calculated out of this by the generator-tools.		
<b>Multiplicity</b>	1		
<b>Type</b>	EcucFloatParamDef		
<b>Range</b>	[1E-8 .. 0.01]		
<b>Default value</b>	1E-6		
<b>Post-Build Variant Value</b>	true		

<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00246 :</b>		
<b>Name</b>	SpiDeviceEcucPartitionRef		
<b>Parent Container</b>	SpiExternalDevice		
<b>Description</b>	Maps an SPI external device to zero or multiple ECUC partitions to limit the access to this external device. The ECUC partitions referenced are a subset of the ECUC partitions where the SPI driver is mapped to.		
<b>Multiplicity</b>	0..*		
<b>Type</b>	Reference to [ EcucPartition ]		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	All Variants
	<b>Link time</b>	--	
	<b>Post-build time</b>	--	
<b>Scope / Dependency</b>	scope: ECU		

**No Included Containers**



**[SWS\_Spi\_CONSTR\_00002]** [ The ECUC partitions referenced by Spi-DeviceEcucPartitionRef shall be a subset of the ECUC partitions referenced by SpiEcucPartitionRef. ]()

**[SWS\_Spi\_CONSTR\_00004]** [ If SpiEcucPartitionRef references one or more ECUC partitions, SpiDeviceEcucPartitionRef shall have a multiplicity of greater than zero and reference one or several of these ECUC partitions as well. ]()

## 10.2.9 SpiDriver

<b>SWS Item</b>	<b>ECUC_Spi_00091 :</b>		
<b>Container Name</b>	SpiDriver		
<b>Parent Container</b>	Spi		
<b>Description</b>	This container contains the configuration parameters and sub containers of the AUTOSAR Spi module.		
<b>Configuration Parameters</b>			

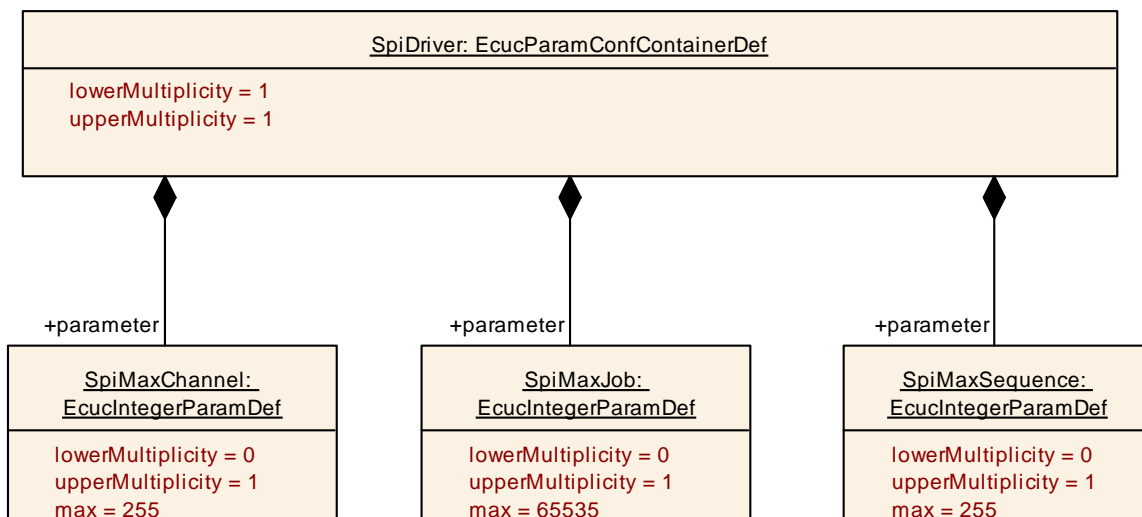
<b>SWS Item</b>	<b>ECUC_Spi_00197 :</b>		
<b>Name</b>	SpiMaxChannel		
<b>Parent Container</b>	SpiDriver		
<b>Description</b>	This parameter contains the number of Channels configured. It will be gathered by tools during the configuration stage.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 255		
<b>Default value</b>	--		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00198 :</b>		
<b>Name</b>	SpiMaxJob		
<b>Parent Container</b>	SpiDriver		
<b>Description</b>	Total number of Jobs configured.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 65535		
<b>Default value</b>	--		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME

	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>SWS Item</b>	<b>ECUC_Spi_00199 :</b>		
<b>Name</b>	SpiMaxSequence		
<b>Parent Container</b>	SpiDriver		
<b>Description</b>	Total number of Sequences configured.		
<b>Multiplicity</b>	0..1		
<b>Type</b>	EcucIntegerParamDef		
<b>Range</b>	0 .. 255		
<b>Default value</b>	--		
<b>Post-Build Variant Multiplicity</b>	true		
<b>Post-Build Variant Value</b>	true		
<b>Multiplicity Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Value Configuration Class</b>	<b>Pre-compile time</b>	X	VARIANT-PRE-COMPILE
	<b>Link time</b>	X	VARIANT-LINK-TIME
	<b>Post-build time</b>	X	VARIANT-POST-BUILD
<b>Scope / Dependency</b>	scope: local		

<b>Included Containers</b>		
<b>Container Name</b>	<b>Multiplicity</b>	<b>Scope / Dependency</b>
SpiChannel	1..*	All data needed to configure one SPI-channel
SpiExternalDevice	1..*	The communication settings of an external device. Closely linked to SpiJob.
SpiJob	1..*	All data needed to configure one SPI-Job, amongst others the connection between the internal SPI unit and the special settings for an external device is done.
SpiSequence	1..*	All data needed to configure one SPI-sequence



### 10.2.10 SpiPublishedInformation

<b>SWS Item</b>	<b>ECUC_Spi_00235 :</b>
<b>Container Name</b>	SpiPublishedInformation
<b>Parent Container</b>	Spi

Description	Container holding all SPI specific published information parameters		
Configuration Parameters			
SWS Item	ECUC_Spi_00236 :		
Name	SpiMaxHwUnit		
Parent Container	SpiPublishedInformation		
Description	Number of different SPI hardware microcontroller peripherals (units/busses) available and handled by this SPI Handler/Driver module.		
Multiplicity	1		
Type	EcucIntegerParamDef		
Range	0 .. 18446744073709551615		
Default value	--		
Post-Build Variant Value	false		
Value Configuration Class	Published Information	X	All Variants
Scope / Dependency	scope: local		
No Included Containers			

## 10.3 Published information

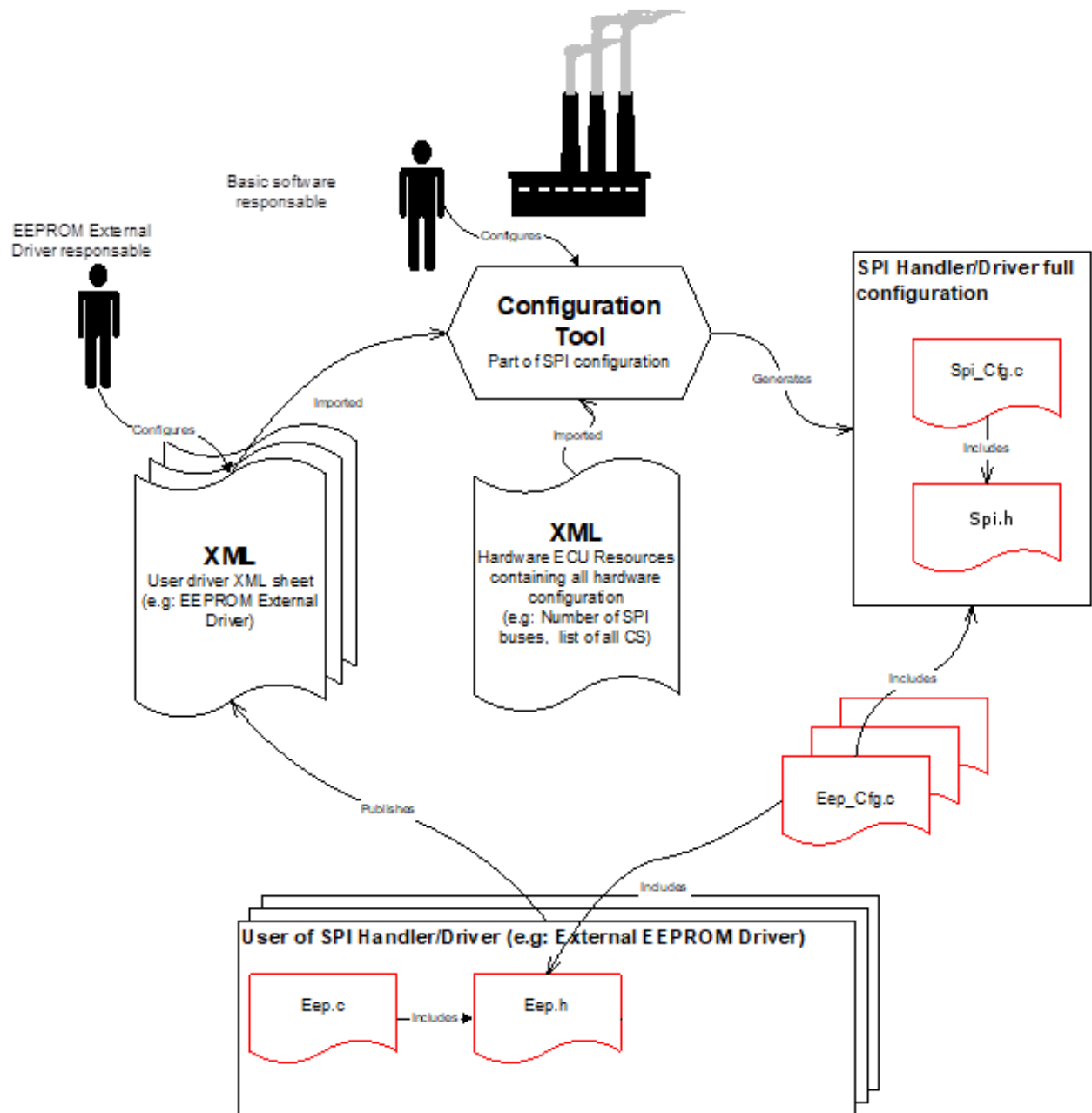
For details refer to the chapter 10.3 “Published Information” in *SWS\_BSWGeneral*

## 10.4 Configuration concept

There is a relationship between the SPI Handler/Driver module and the modules that use it. This relationship is resolved during the configuration stage and the result of it influences the proper API and behaviour between those modules.

The user needs to provide to the SPI Handler/Driver part of the configuration to adapt it to its necessities. The SPI Handler/Driver shall take this configuration and provide the needed tools to the user.

The picture shows the information flow during the configuration of the SPI Handler/Driver. It is shown only for one user, using an External EEPROM Driver as example, but this situation is common to all users of the SPI Handler/Driver. To highlight the situation where more users are affected, several overlapping documents are drawn.



The steps on the diagrams are:

1. The user (External EEPROM Driver) of SPI Handler/Driver edits a XML configuration file. This XML configuration file is the same used by the user to generate its own configuration.
2. For each ECU, a XML HW configuration document contains information which should be used in order to configure some parameters.
3. The “SPI generation tool”. The Generation tool (here is reflected only the part that generates code to SPI usage) shall generate the handles to export and the instance of the configuration sets. In this step the software integrator will provide missing information.
4. SPI instance configuration file. As a result of the generation all the symbolic handlers needed by the user are included in the configuration header file of the SPI Handler/Driver.

5. User gets the symbolic name of handlers. User imports the handle generated to make use of them as requested by its XML configuration file.

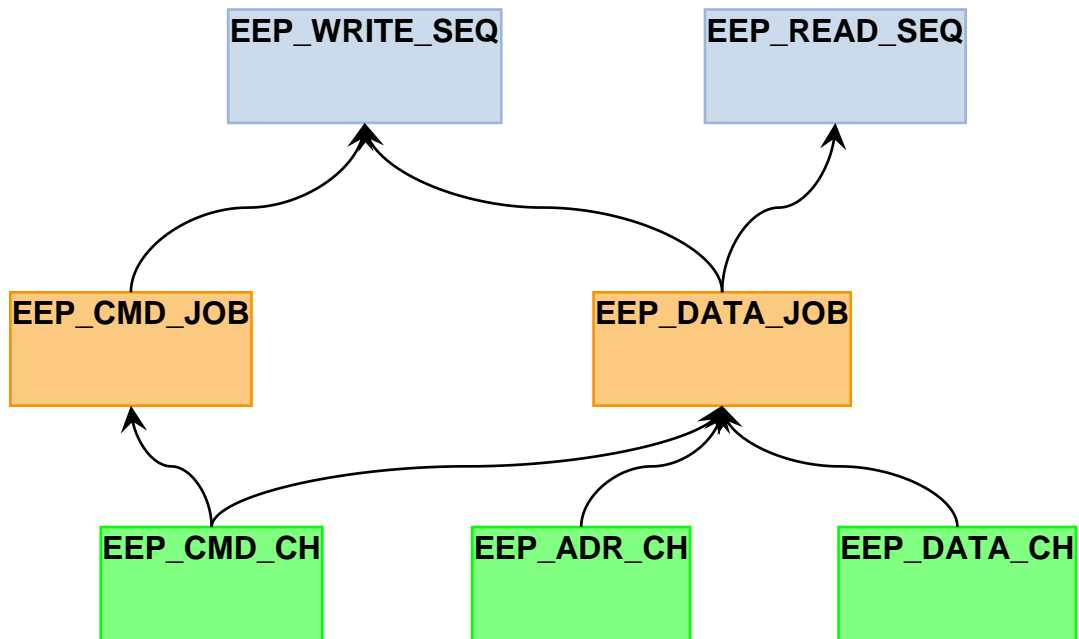
## 11 Not applicable requirements

**[SWS\_Spi\_00999]** 「 These requirements are not applicable to this specification. 」

(SRS\_BSW\_00301, SRS\_BSW\_00302, SRS\_BSW\_00306, SRS\_BSW\_00307, SRS\_BSW\_00308, SRS\_BSW\_00309, SRS\_BSW\_00312, SRS\_BSW\_00325, SRS\_BSW\_00328, SRS\_BSW\_00330, SRS\_BSW\_00331, SRS\_BSW\_00334, SRS\_BSW\_00341, SRS\_BSW\_00342, SRS\_BSW\_00343, SRS\_BSW\_00347, SRS\_BSW\_00375, SRS\_BSW\_00399, SRS\_BSW\_00400, SRS\_BSW\_00401, SRS\_BSW\_00413, SRS\_BSW\_00416, SRS\_BSW\_00417, SRS\_BSW\_00422, SRS\_BSW\_00423, SRS\_BSW\_00424, SRS\_BSW\_00426, SRS\_BSW\_00427, SRS\_BSW\_00428, SRS\_BSW\_00429, SRS\_BSW\_00432, SRS\_BSW\_00433, SRS\_BSW\_00005, SRS\_BSW\_00006, SRS\_BSW\_00009, SRS\_BSW\_00010, SRS\_BSW\_00161, SRS\_BSW\_00164, SRS\_BSW\_00168, SRS\_BSW\_00170, SRS\_BSW\_00172, SRS\_SPAL\_12267, SRS\_SPAL\_12068, SRS\_SPAL\_12069, SRS\_SPAL\_12063, SRS\_SPAL\_12129, SRS\_SPAL\_12067, SRS\_SPAL\_12077, SRS\_SPAL\_12078, SRS\_SPAL\_12092, SRS\_SPAL\_12265)

## 12 Appendix

The table shown on the next page is just an example to help future users (and/or developers) that have to configure software modules to use the SPI Handler/Driver. This table is independent of the `Spi_ConfigType` structure but contains all elements and aggregations like Channels, Jobs and Sequences.



External EEPROM Write/Read Configuration for SPI Handler/Driver								
Sequences			Jobs			Channels		
Symbolic Name	ID	Attributes	Symbolic Name	ID	Attributes	Symbolic Name	ID	Attributes
EEP_WRITE_SEQ	0	2 (Number of Jobs), {EEP_CMD_JOB, EEP_DATA_JOB} (List of Jobs), Not Interruptible, EEP_vidEndOfWriteSeq	EEP_CMD_JOB	0	SPI_BUS_0, CS_EEPROM, CS_ON, CS_LOW, CLK_2MHz, 1 (time in $\mu$ s), Polarity 180, Falling Edge, 3, EEP_vidEndOfStartWrJob, 1 (Number of Channels) {EEP_CMD_CH} (List of Channels)	EEP_CMD_CH	0	EB, 8 bits, 1 data to TxD, MSB First, Default value is 0x00
EEP_READ_SEQ	1	1 (Number of Jobs), {EEP_DATA_JOB} (List of Jobs), Not Interruptible, EEP_vidEndOfReadSeq	EEP_DATA_JOB	1	SPI_BUS_0, CS_EEPROM, CS_ON, CS_LOW, CLK_2MHz, 1 (time in $\mu$ s), Polarity 180, Falling Edge, 2, NULL, 3 (Number of Channels) {EEP_CMD_CH, EEP_ADR_CH, EEP_DATA_CH} (List of Channels)	EEP_ADR_CH	1	EB, 16 bits, 1 data to TxD, MSB First, Default value is 0x0000
						EEP_DATA_CH	2	EB, 8 bits, 32 data to TxD, MSB First, Default value is 0x00